

LV40100CT

Rev. H Oct.-2018

TO-553 Ț

Schottky Barrier Diode in a TO-220 Plastic Package.

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Low forward voltage drop,low poer losses,High efficiency operation.

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OR-ing

GF-GF Ț

For use in high frequency inverters, switching

Parameter	N Symbol	Rating	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RM} V_{RSM} V_{DC}	100	V
RMS Reverse Voltage	V_{RMS}	70	V
Average Rectified Forward Current	$I_{F(AV)}$	2×20	A
Non Repetitive Peak Surge Current	I_{FSM}	300	A
Thermal Resistance Junction to Case	$R_{\theta Jc}$	2.8	/W
Junction and Storage Temperature Range	T_j T_{stg}	-40° +150	

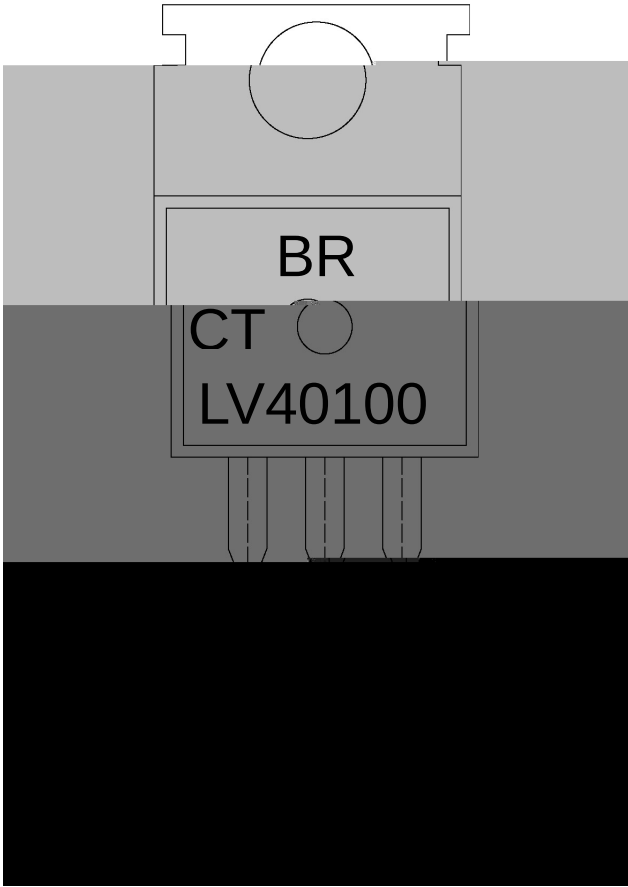
Parameter	N Symbol	Test Conditions	Min	Typ	Max	Unit
Reverse Voltage	V_{BR}	$I_R=1mA(T_a=25^\circ C)$	100			V
Forward Voltage	V_F	$I_F=5A(T_a=25^\circ C)$		0.45		V
		$I_F=10A(T_a=25^\circ C)$		0.54		V
		$I_F=20A(T_a=25^\circ C)$		0.67	0.75	V
		$I_F=5A(T_a=125^\circ C)$		0.38		V
		$I_F=10A(T_a=125^\circ C)$		0.49		V
		$I_F=20A(T_a=125^\circ C)$		0.62	0.65	V
Instantaneous Reverse Current	I_R	$V_R=100V(T_a=25^\circ C)$ ~ Note 1 ~ $V_R=100V(T_a=125^\circ C)$		30	200	μA

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DATA SHEET

1. 1.1 / Marking Instructions

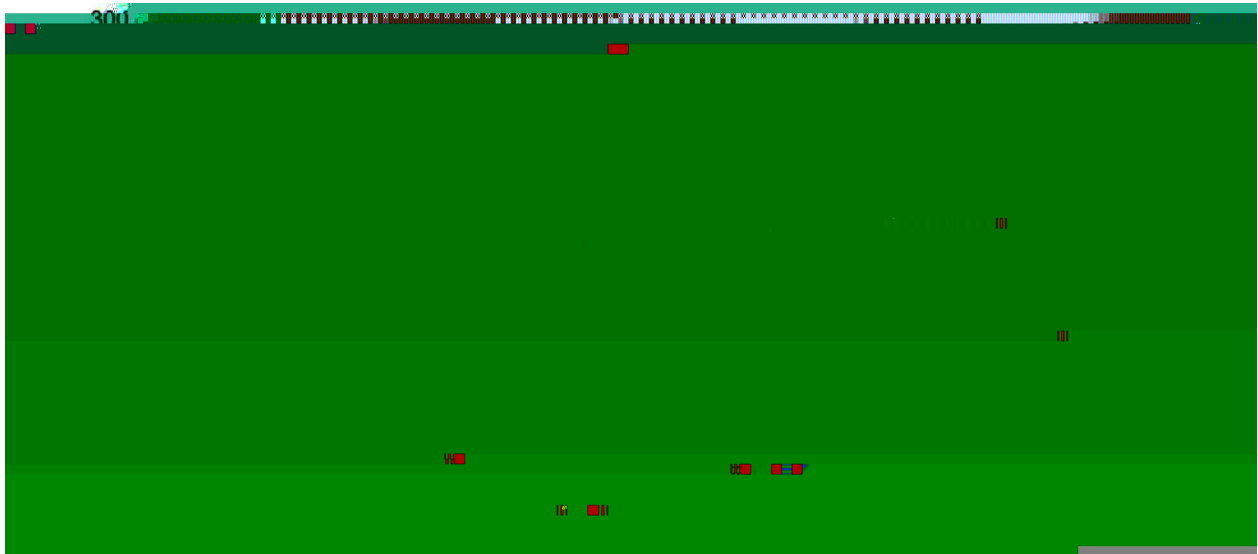


ER
LV43433
FT:

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Note:
BR: Company Code
LV40100 Product Type.
CT: Internal Structure
****: Lot No. Code, code change with Lot No.

↓ § ¶ ŵ'E ¶ f(ŵ'ŝ) / Temperature Profile for Dip Soldering(Pb-Free)



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- 1 أ 25 150 ج 60 90sec; Note: 1.Preheating:25~150 , Time:60~90sec.
- 2 255±5 ج 5±0.5sec; 2.Peak Temp.:255±5 , Duration:5±0.5sec.
- 3 2 10 /sec. 3. Cooling Speed: 2~10 /sec.

↓ ŵ q ŵ'Y ¶ / Resistance to Soldering Heat Test Conditions

270±5 ج 10±1 sec. Temp.:270±5 Time:10±1 sec

¶ ŵ ŵ' / Packaging SPEC.

ŵ / BULK

Package Type	Units	Dimension	(unit mm ³)
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