

5 é / Descriptions

9 5 : .> // x 4 6 4 ! - } â 'ož Silicon NPN transistor in a SOT-23 Plastic Package.

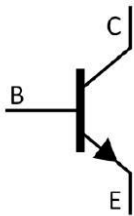
□^a / Features

2 3 ; mož
Complementary pair with L8550M.

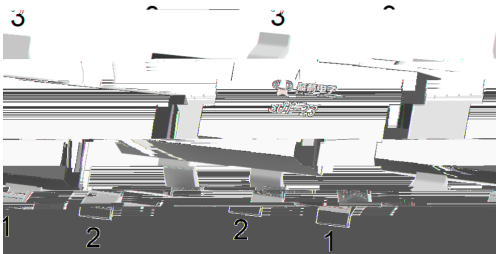
Đ ÷ / Applications

9 ä y Ý èož
Power amplifier applications.

Ã W] Ô · / Equivalent Circuit



• Ů - æ

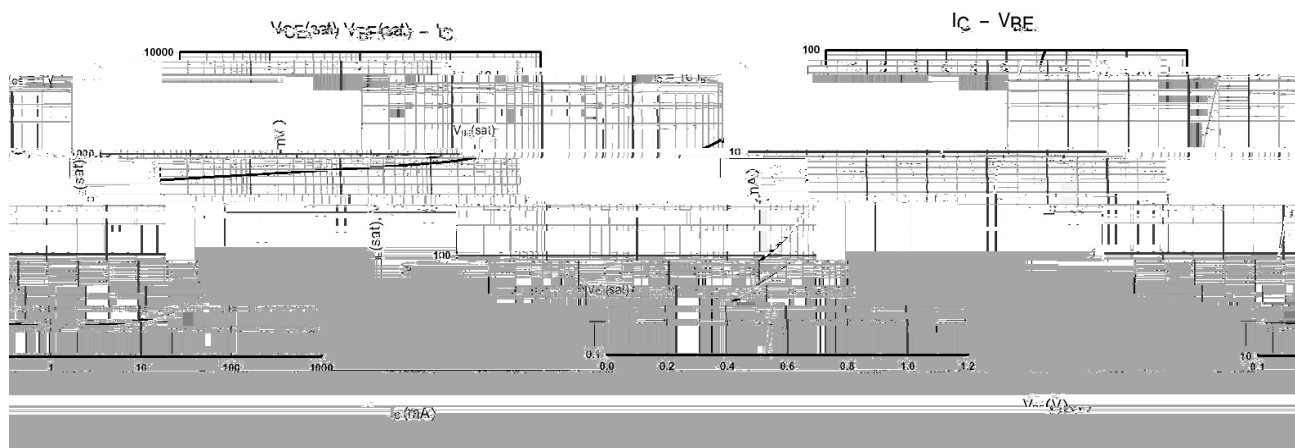
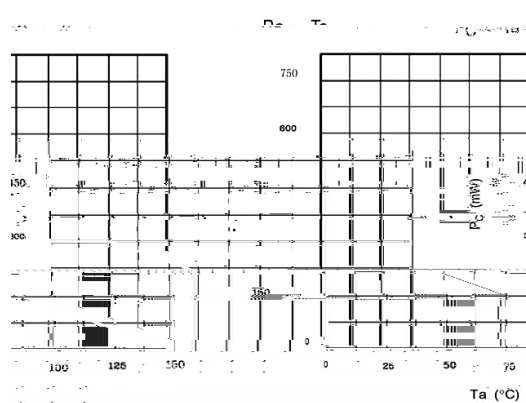
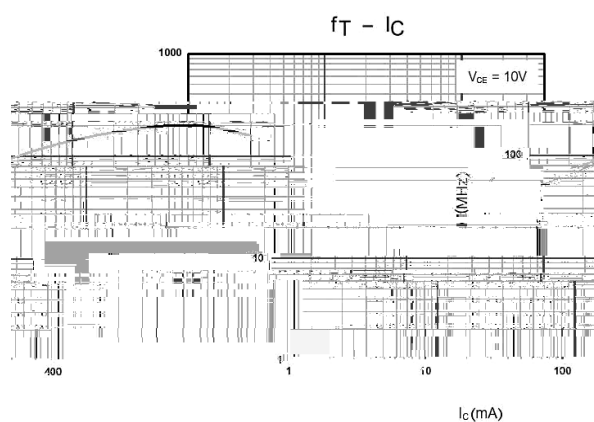


PIN1 y Base PIN 2 y Emitter PIN 3 y Collector

Z Ü B , M V / h_{FE} Classifications & Marking

<div>@ f</div> <div>Parameter</div>	<div>... Z</div> <div>Symbol</div>	<div>f ></div> <div>Rating</div>	<div>% y</div> <div>Unit</div>
Collector to Base Voltage	V		

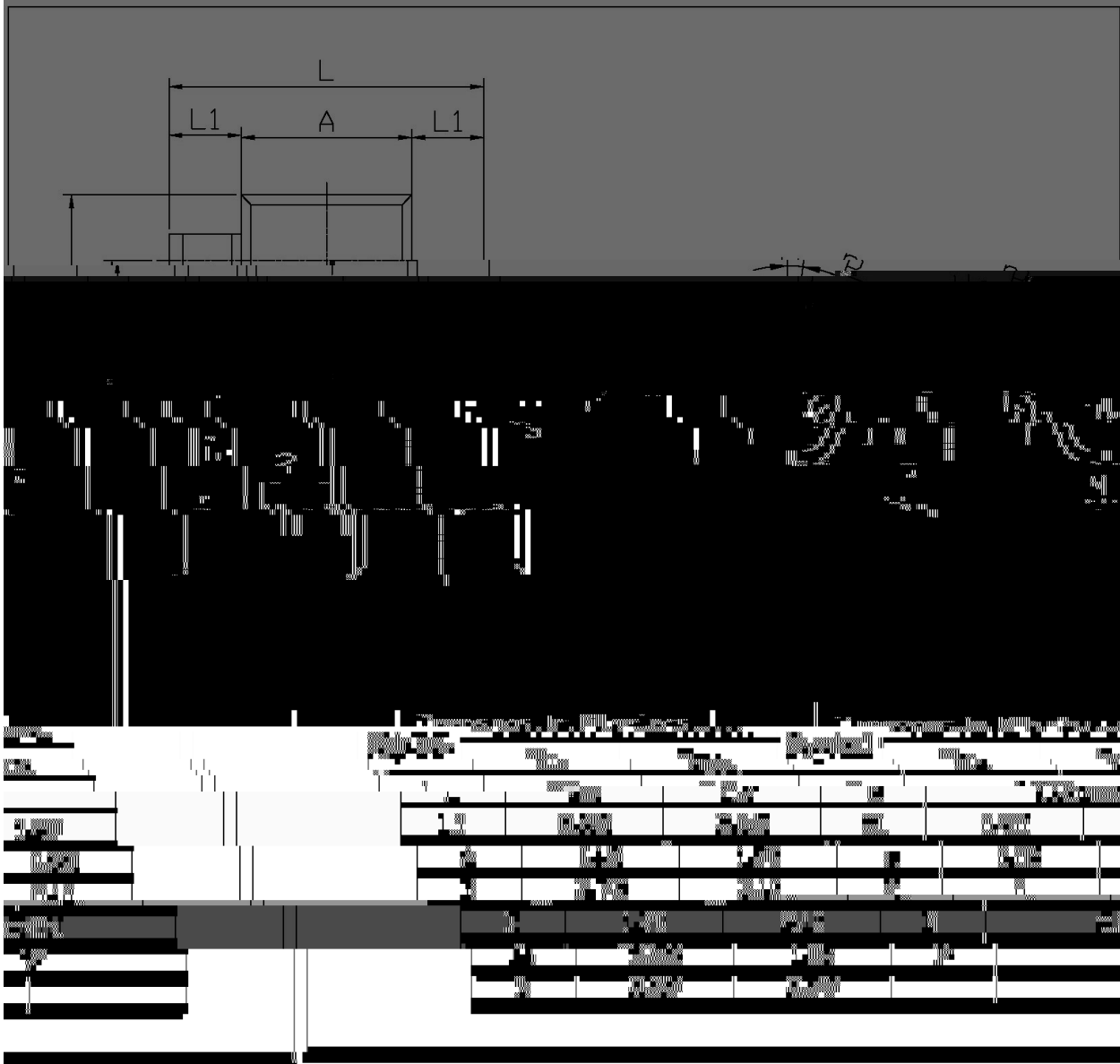
〇 ? d • Ž ¢ / Electrical Characteristic Curve



Ø □ =) ⌀ / Package Dimensions

SOT-23

单位: mm



šWD t... • Žč (x/) / :KSVKXGZ[XK 6XULORK LUX /8 8KLRU] 9URJKXOTM 6

ª ¢ y

1o• Ä ½ “† 150 ½180 - k ž • 60 ½90sec;
2o• Q › “† 245 r5 - k ž • 4 Ò 5 r0.5sec;
3o•D N ò i Ò 0 ,† 2 ½10 - /sec.

Note:

1.Preheating:150~180 - , Time:60~90sec.
2.Peak Temp.:245 r5 - , Duration:5 r0.5sec.
3. Cooling Speed: 2~10 - /sec.

ÂD /Cã p¹⁰»] / Resistance to Soldering Heat Test Conditions

“†y 260 r5 - ž • y 10 r1 sec. Temp.:260 r5 - Time:10 r1 sec

G P á / Packaging SPEC.

2 & x / REEL