/ Descriptions

VRS0; ṂṂ Ḭ Q ॐ MOS ʃ ῶ Ẓ ٦ N-Channel MOSFET in a SOP-8 Plastic Package.

/ Features

Ḥ Ṁ ِ ɔ ﮓ $U_{GV\,\varepsilon\,RQ\,\zeta}$ ı Ḥ ‖ ɔ U ı Ч ᵽ ̋ ð ৯ ჯ ̈Ω ≠ ό ı ٧ ᵀ UrKV

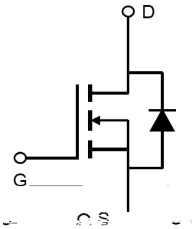
Low $R_{DS(ON)}$, Low Gate Charge, Optimized for fast-switching, RoHS.

/ Applications

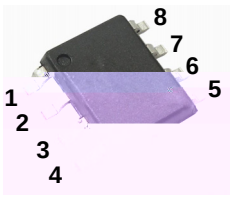
GF2GF ⊐ DF2GF ჯ ̈Ω н [OB] a ℓ ̃Ǫ ╚ ı ڭ ̄ ´ ╚2´ ╚ ჯ ̈Ω н ƍ ɔ ʒ ⊐ Ṫ ў

Synchronus Rectification in DC/DC and AC/DC Converters, Isolated DC/DC Converters in Telecom and Industrial.

/ Equivalent Circuit



/ Pinning



PIN1:S PIN 2:S PIN 3 χ S PIN 4 χ G

PIN5 PIN 6 PIN 7 PIN 8:D

/ h_{FE} Classifications & Marking

ð м ٧ ب ″ See Marking Instructions.

/ Electrical Characteristics(Ta=25)

v ̈Q Parameter	ϒ N Symbol	⅄ ;ǂ ϯ Test Conditions	ѡ ṇ Ө Min	Ɛ ʒ Ө Typ	ѡ ᶻ Ө Max	ɒ Ꮒ Unit
Input Capacitance	C_{iss}	V_{DS}=30V V_{GS}=0V f=1.0MHz		1340		pF
Output Capacitance	C_{oss}			123		
Reverse Transfer Capacitance	C_{rss}			10		
Gate resistance	R_g	f=1.0MHz	0.7	1.5	2.3	Ϝ
Total Gate Charge(10V)	Q_g	V_{DD}=10V I_D=10A V_{DS}=30V		21	30	nC
Total Gate Charge(4.5V)				9.0	15	
Gate-Source Charge	Q_{gs}			4.7		
Gate-Drain Charge	Q_{gd}			2.6		
Turn-On Delay Time	tRTj10.5 0 0 10.5 283.415.87042Tj7.02 0 0 Tc-0GENV					

/ Electrical Characteristic Curve

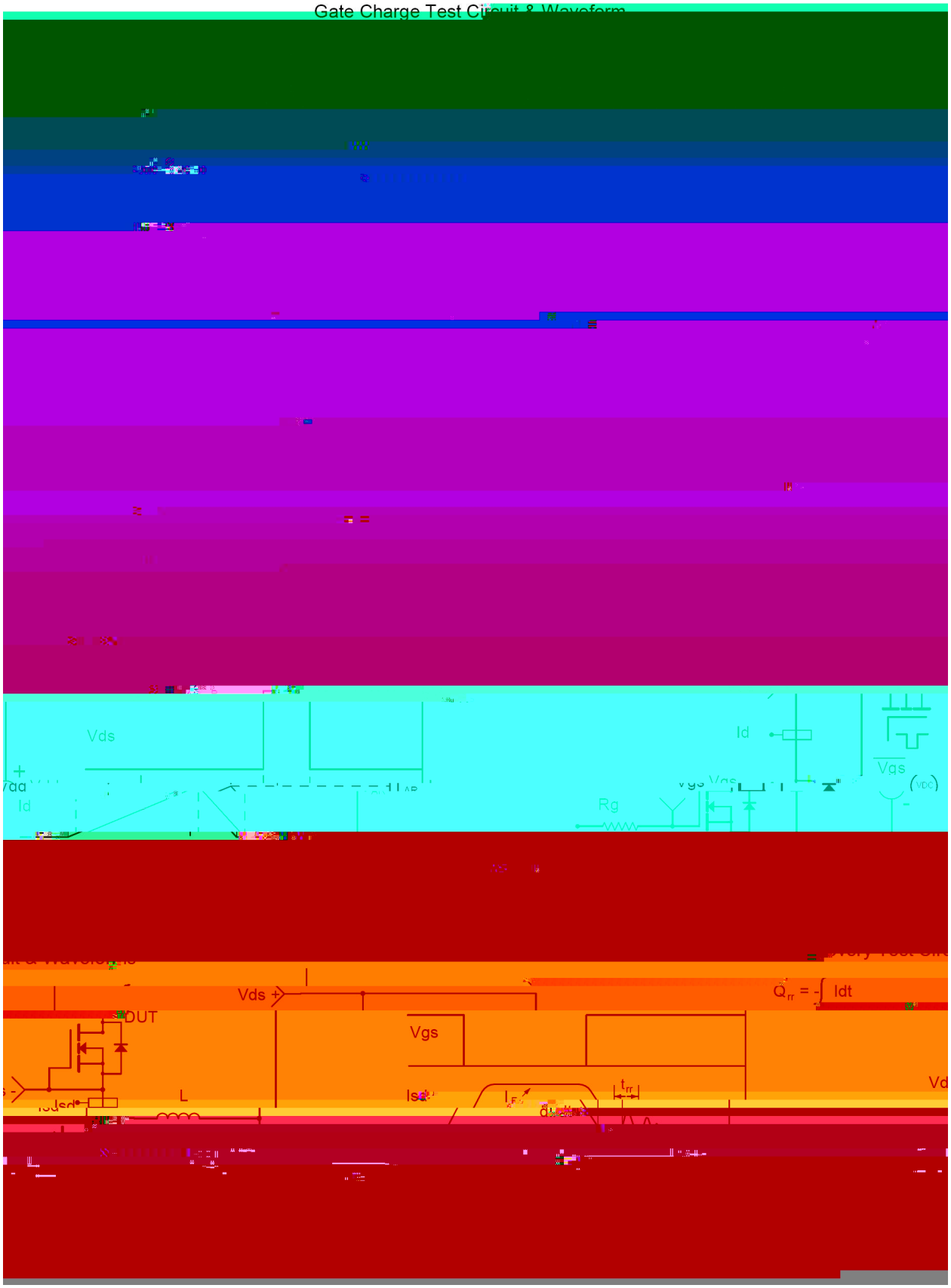


/ Electrical Characteristic Curve



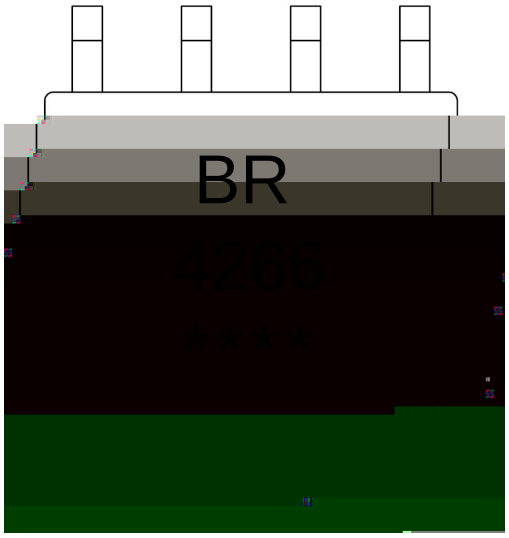
/ Test Circuit & Waveform

Gate Charge Test Circuit & Waveform



/ Package Dimensions

/ Marking Instructions

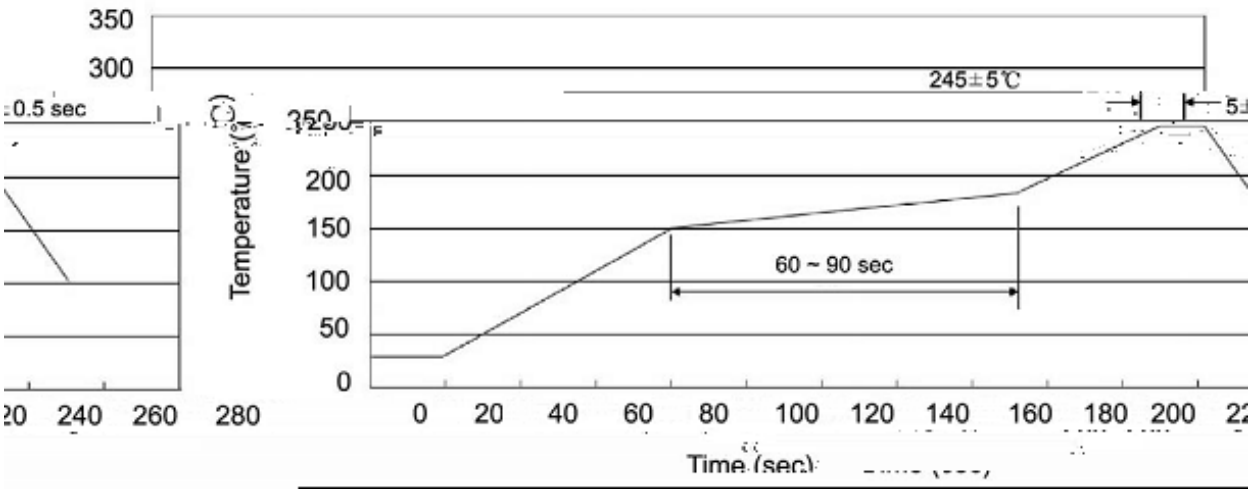


ِ ّ χ	
BR χ	ѭ ϛ ᴎ ᒣ
7599 χ	ѭ ʒ ᴺ ᒣ
---- χ	ѭ ɨ ғ ӈ ᴺ ᒣ ɪ ₹ ɨ ғ ӈ ᴺ Ψ ᖘ

Note:

BR:	Company Code.
4266 χ	Product Type.
****:	Lot No. Code, code change with Lot No.

( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note: