

/ Descriptions

DFN2×2C-6L P+P MOS

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/ Features

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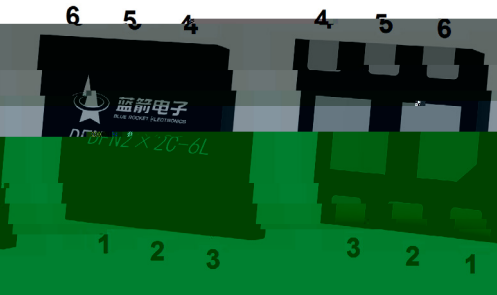
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/ Applications

/ Equivalent Circuit



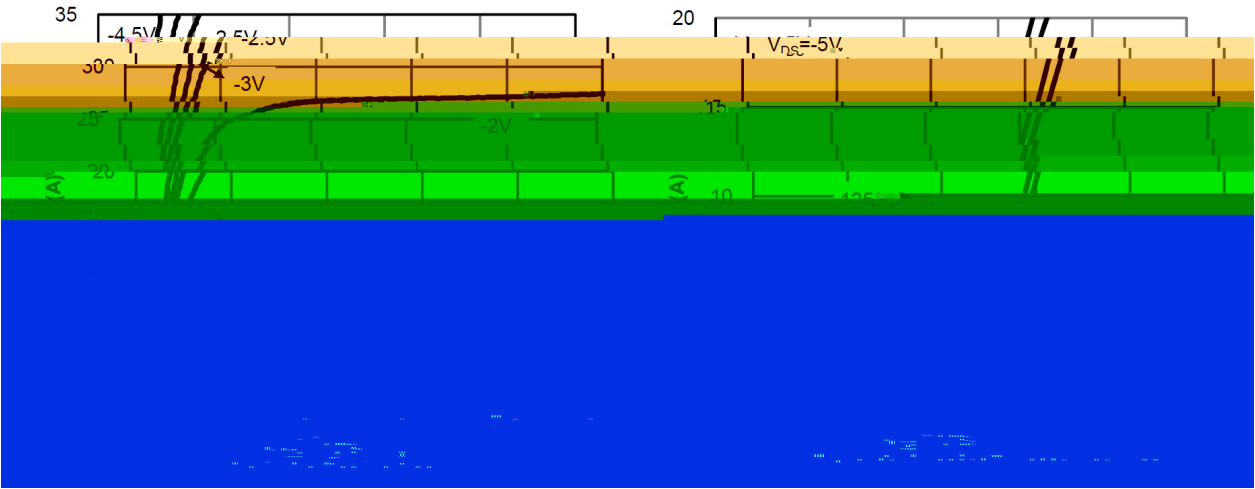
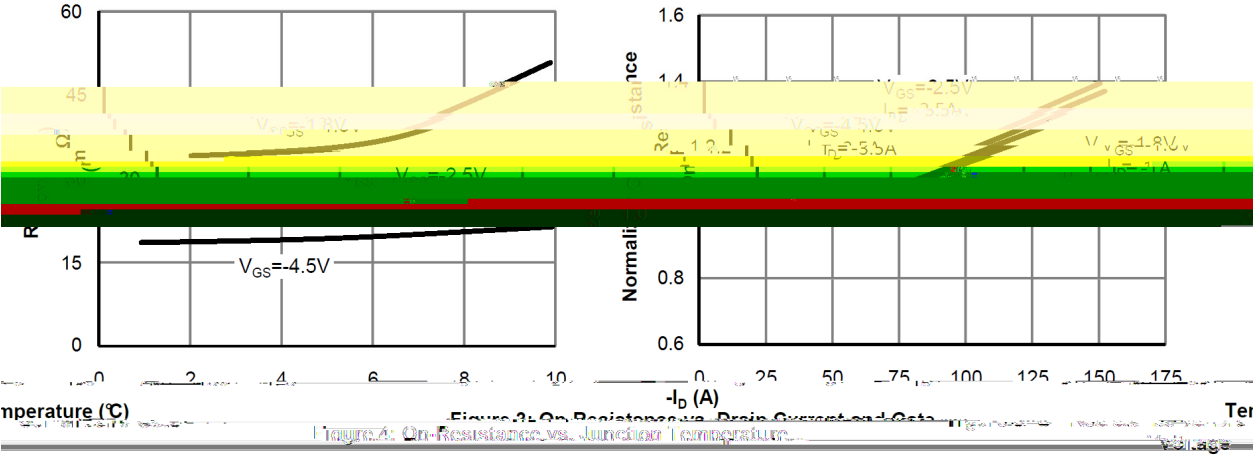
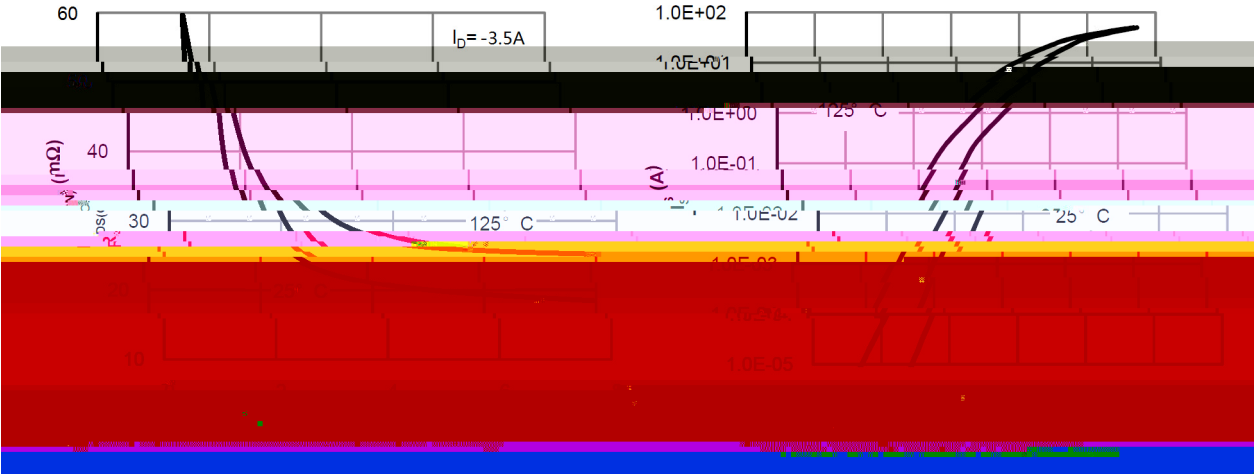
/ Pinning



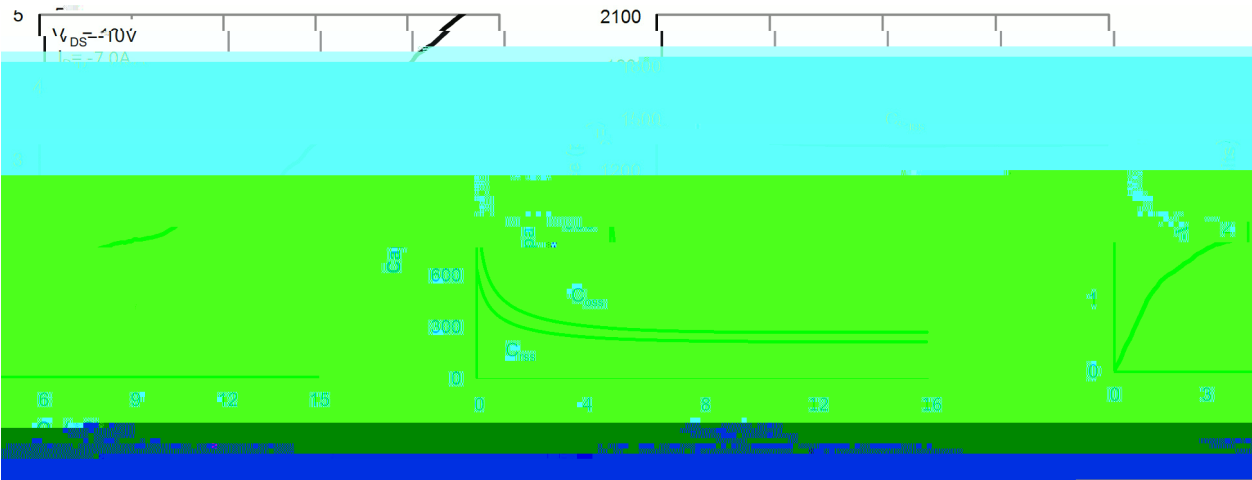
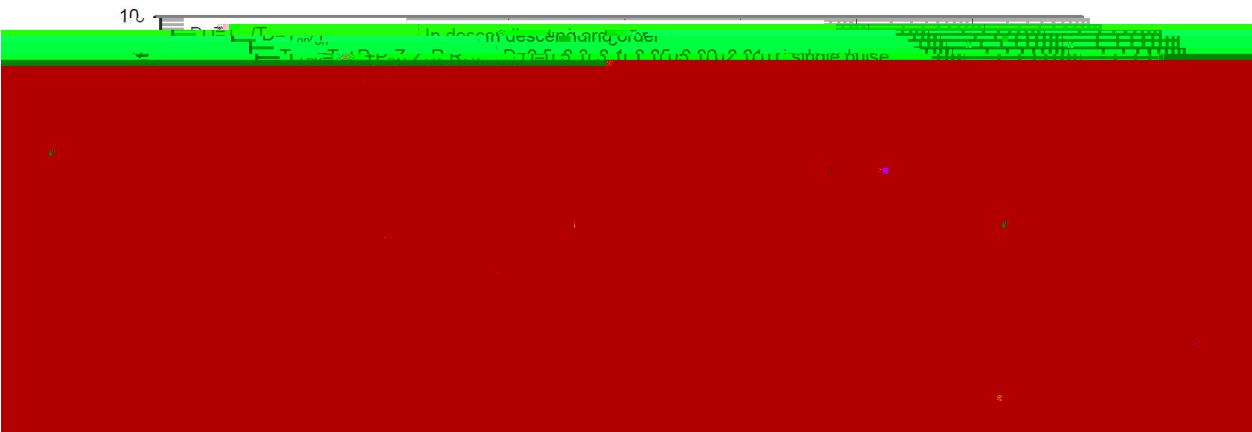
出脚	定义
PIN 1	S1
PIN 2	G1
PIN 3	D2
PIN 4	S2
PIN 5	G2
PIN 6	D1

/ Marking

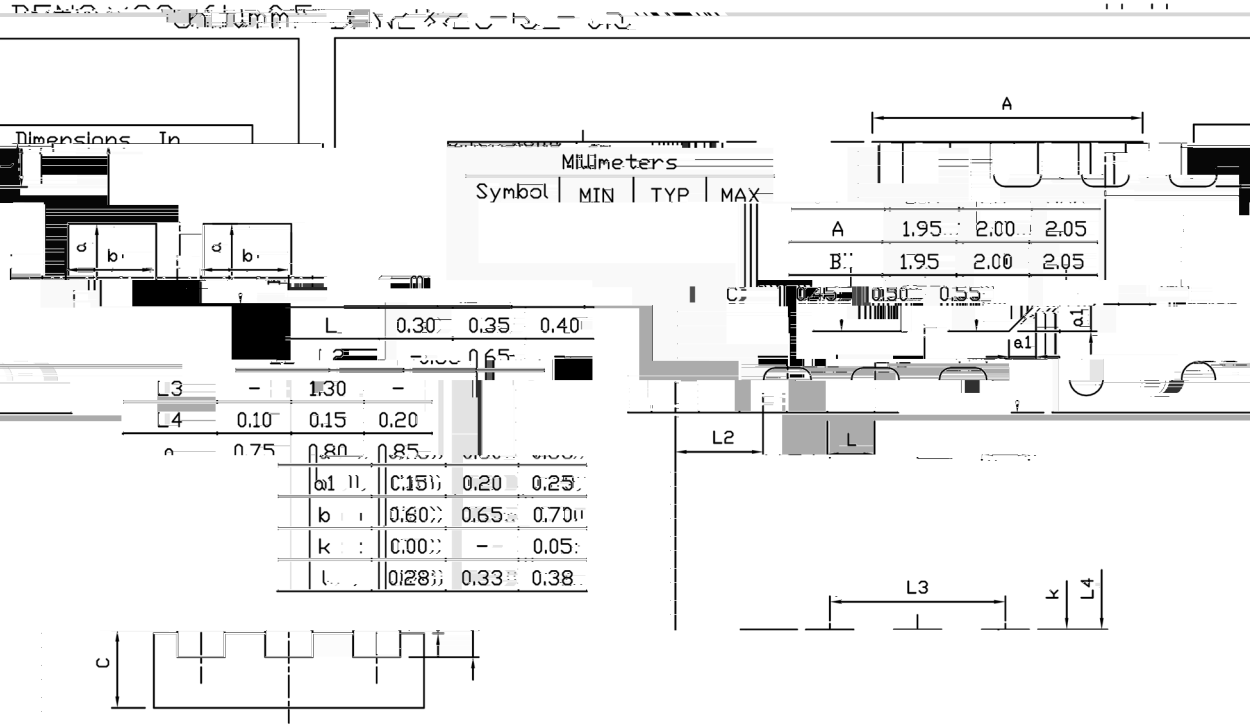
/ Electrical Characteristic Curve



/ Electrical Characteristic Curve



/ Package Dimensions



/ Marking Instructions

