

BRCS100N06BD

/ Absolute Maximum Ratings(Ta=25)

	θ		
	θ		

/ Electrical Characteristics(Ta=25)

						Ω

BRCS100N06BD

Electrical Characteristic Curve

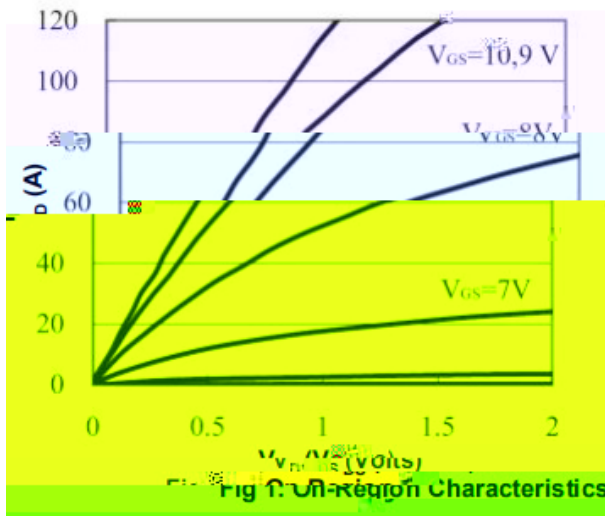
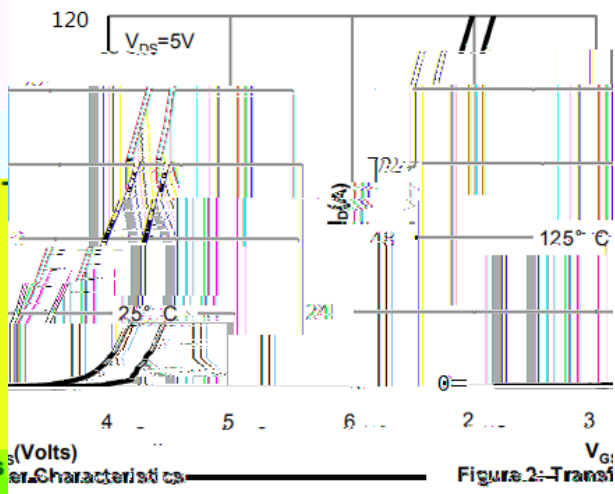
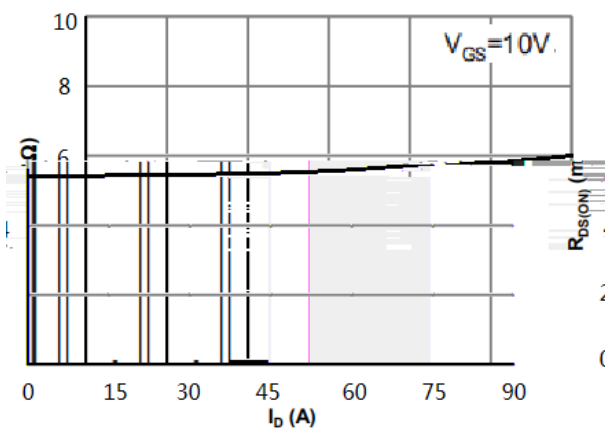
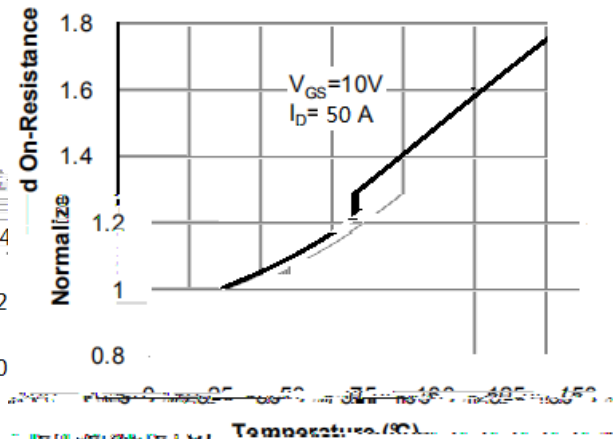
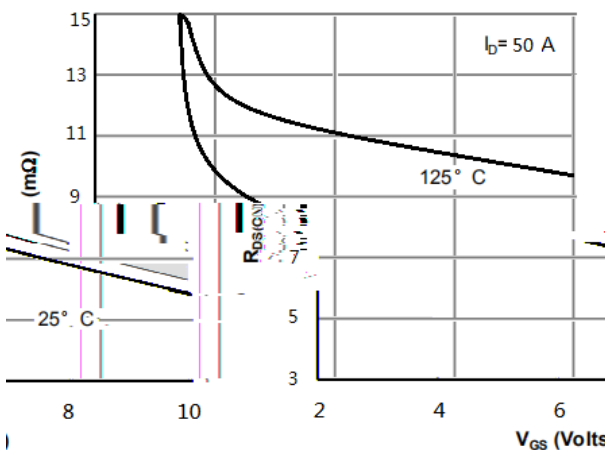
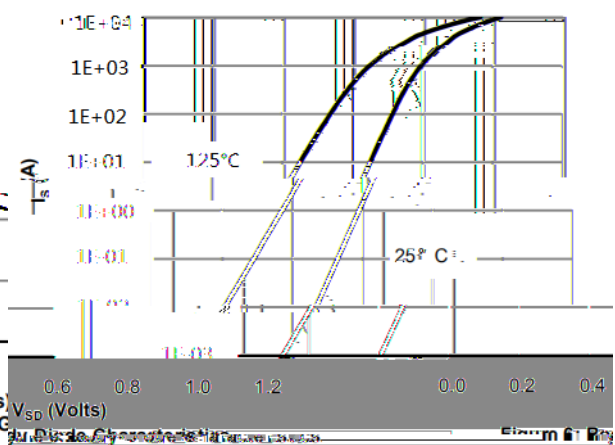
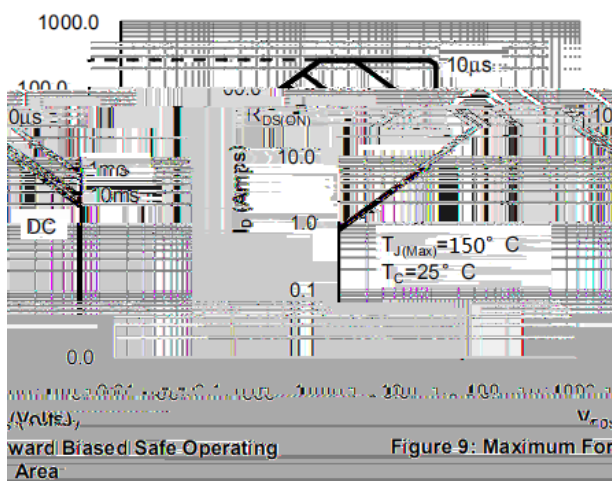
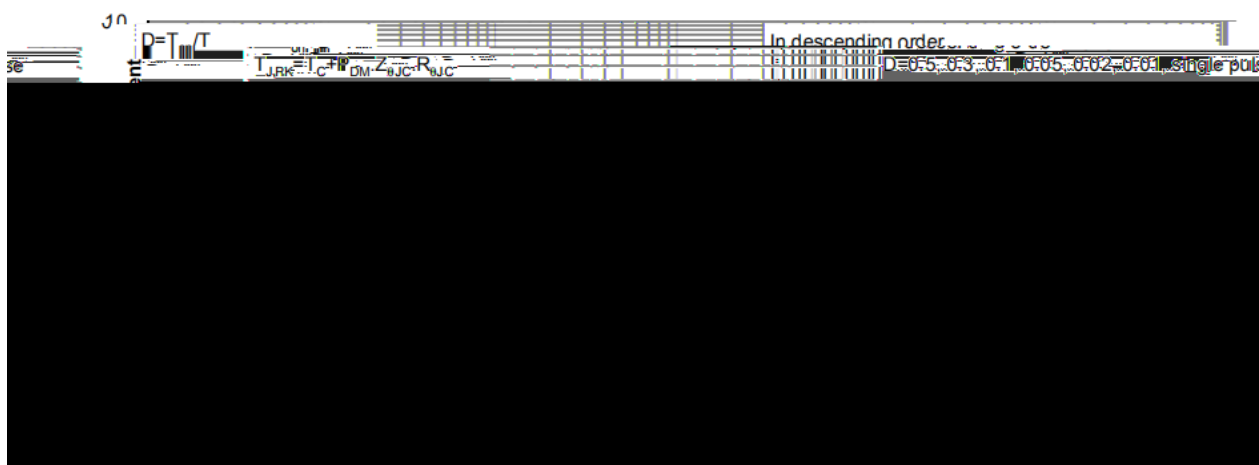
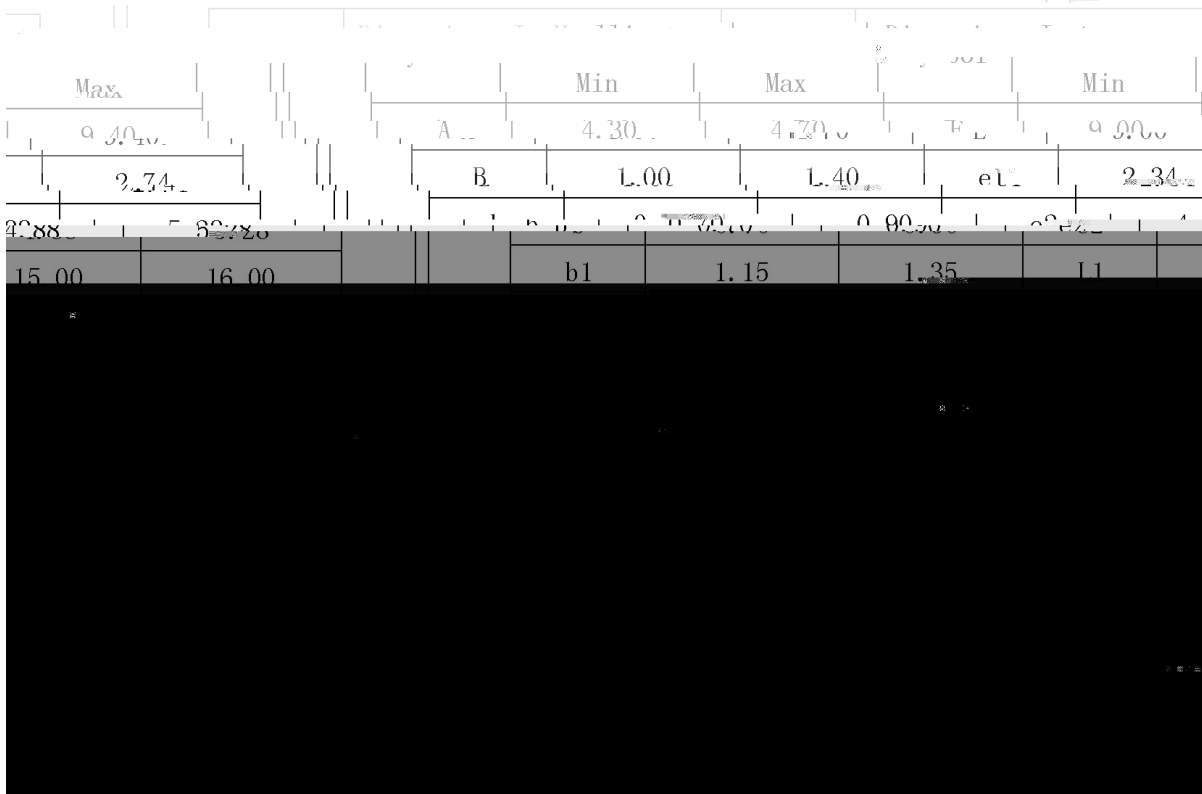


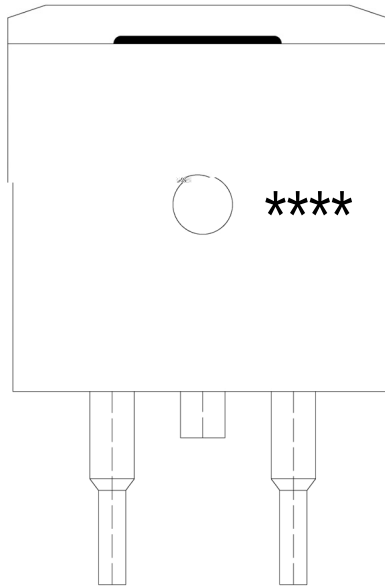
Figure 4: Measured gate charge, gate capacitance, and input capacitance. The figure consists of three subplots. The left subplot shows gate charge (nC) vs. V_{gs} (V) for $V_{ds}=30V$ and $I_d=50A$, with a gate charge of 8 nC marked. The middle subplot shows V_{gs} (%) vs. Q_g (nC) with a gate charge of 8 nC marked. The right subplot shows capacitance (pF) vs. V_{ds} (Volts) for C_{iss} , C_{oss} , and C_{rss} , with C_{iss} marked at 5000 pF.



/ Package Dimensions

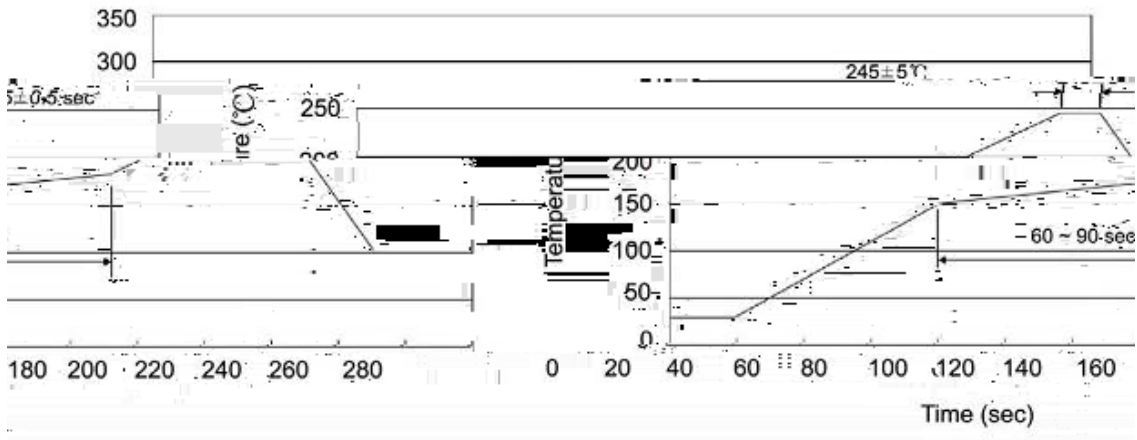


/ Marking Instructions



100N06

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

g m ô h				g m d Ê !	
β m ù ´	Unit Reel / / i	Reel / i / ç	Unit / ç		