

BRCM24C32SC



DATA SHEET

BRCM24C32SC SOP-8 32 Kbit I²C

1.7V	2.5~5.5V	1Mhz	1.7~2.5V
400Khz			
CMOS	400uA	1.6mA	

/ Pinning

Pin	Name	Type	Description
1	E0	Input	
2	E1	Input	
3	E2	Input	
4	GND	Ground	
5	SDA	I/O	/
6	SCL	Input	
7	WCB	Input	
8	VCC	Power	

/ Marking

/ Absolute Maximum Ratings(Ta=25)

			mA
			V

/ Reliability Characteristic

		≤	≤ ≤	
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/ Functional Description

/ Data Input

SDA

SDA

SCL

(1) SCL

SDA

(1)

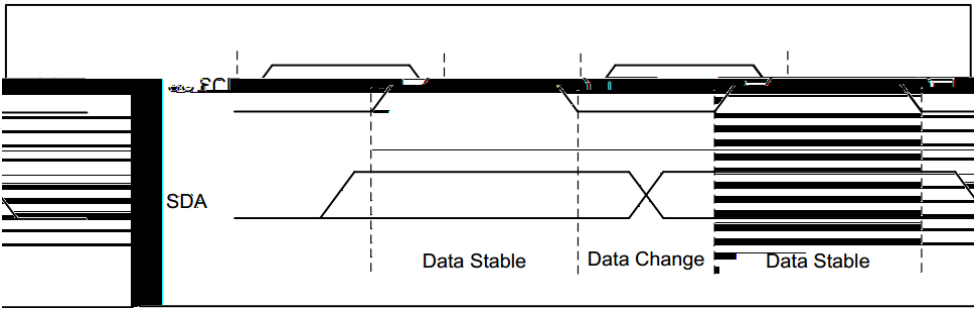


Figure 1 Data Validity

/ Start Condition

SCL

SDA

2

/ Stop Condition

SCL

SDA

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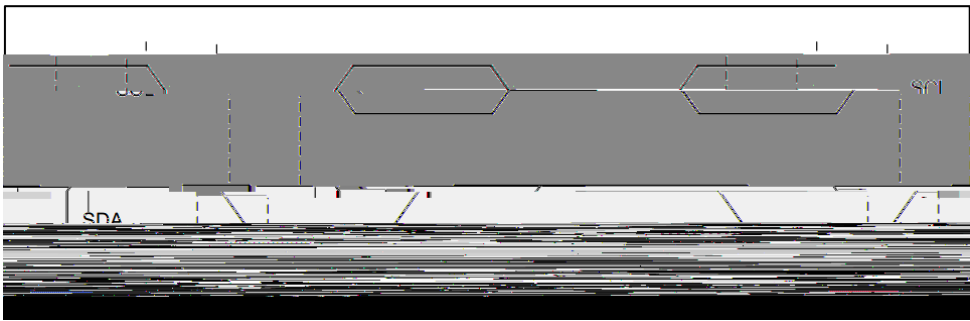


Figure 2 Start and Stop Definition

ACK / Acknowledge

ACK

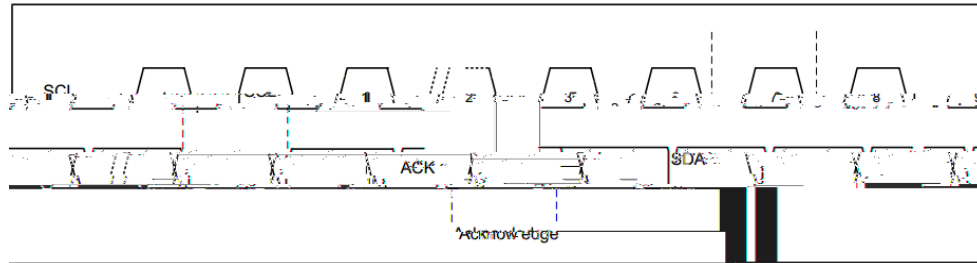
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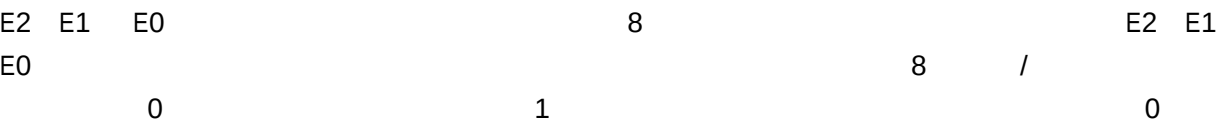
“ 0 ”

9

/ Functional Description



/ Functional Description



/ Data Security

BRCM24C32SC WCB Vcc

/ Byte Write



/ Functional Description

/ Acknowledge Polling

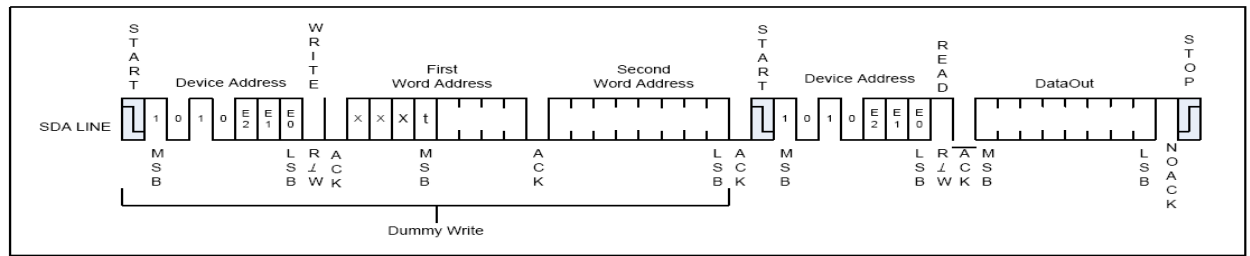
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/ Functional Description



Notes: [1] x means don't care bits.

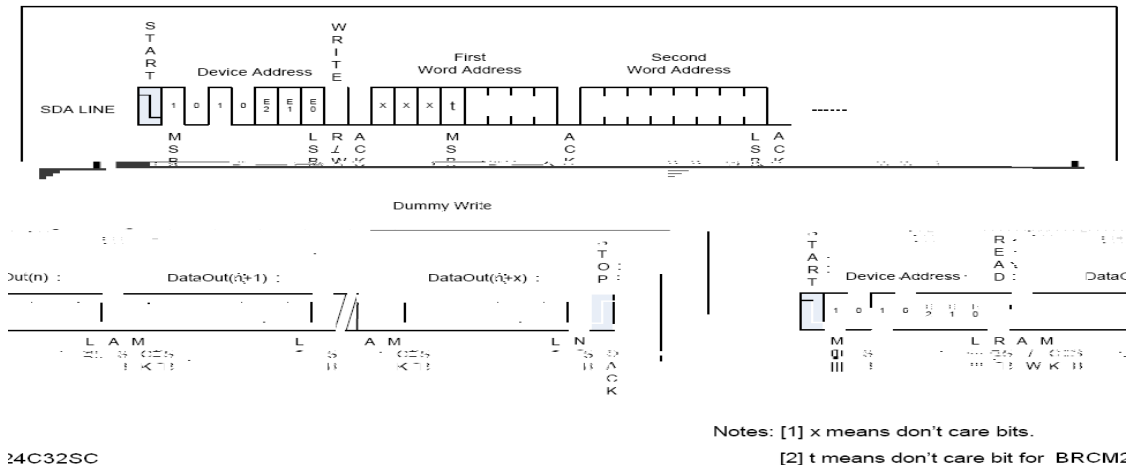
Figure 8 Random Address Read

/ Sequential Read

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“ 0 ”

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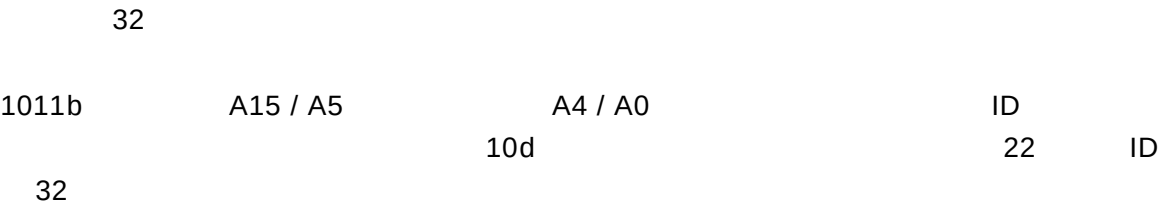
Notes: [1] x means don't care bits.

[2] t means don't care bit for BRCM2

Figure 9

/ Functional Description

/ Read Identification Page



/ Read the Lock Status

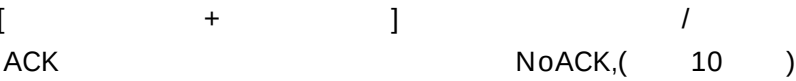
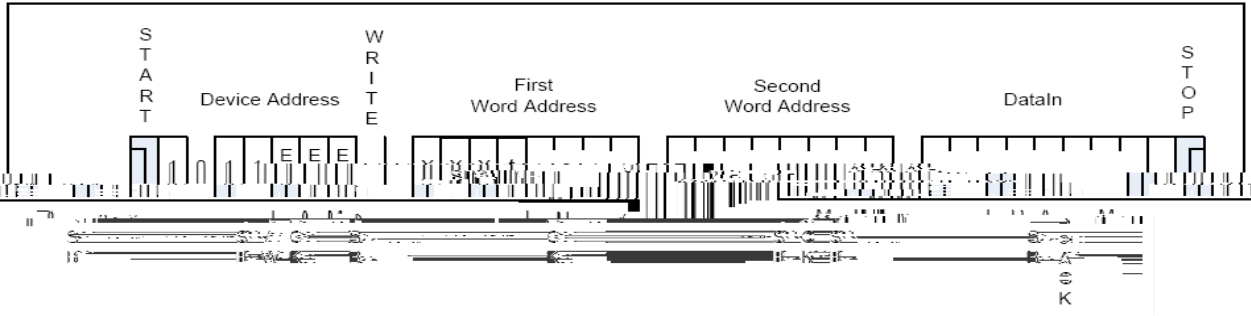
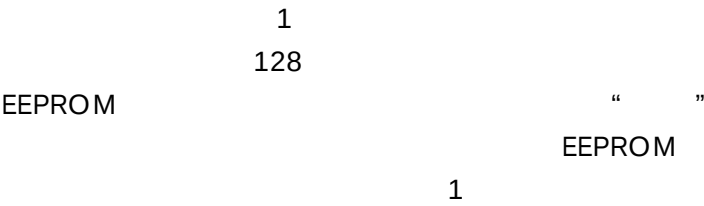


Figure 10 Lock Status Read (When Identification page locked, return NoACK after one data byte)

/ Read Serial Number

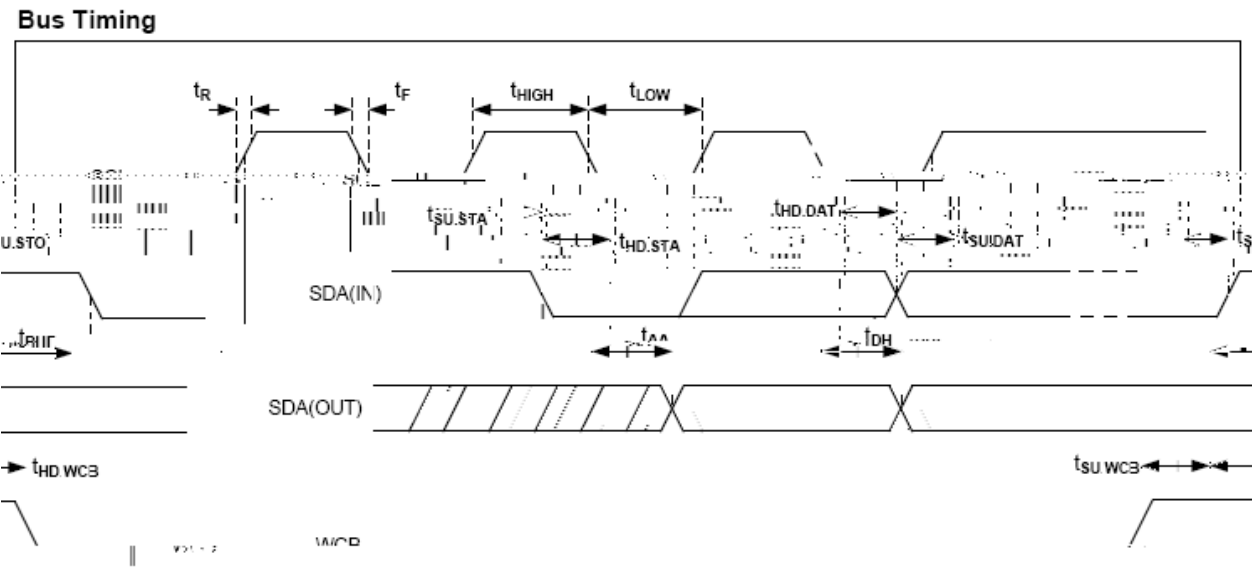


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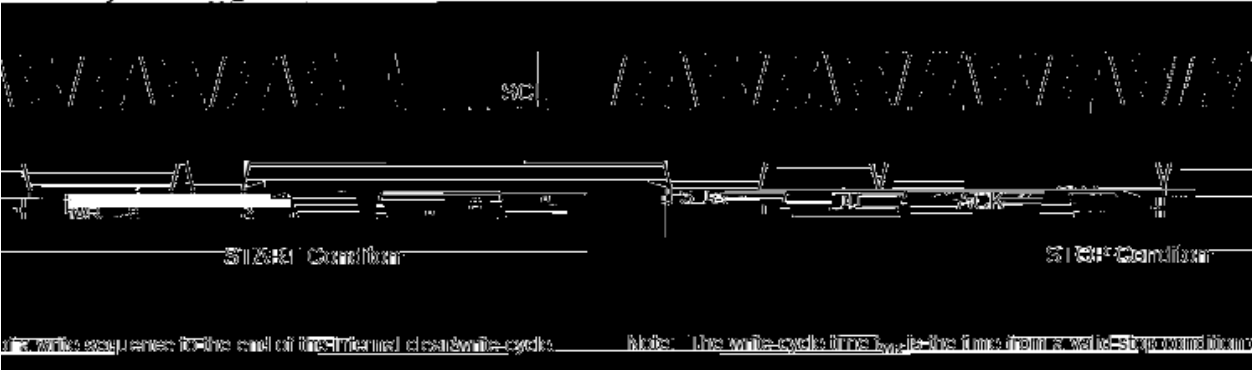


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/ Time Sequence Diagram



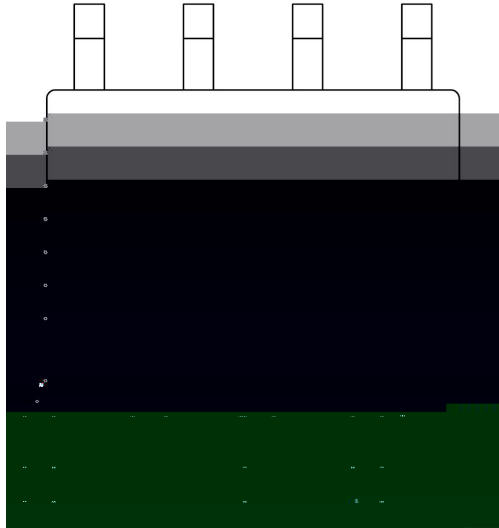
Write Cycle Timing



/ Package Dimensions



/ Marking Instructions



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