

BRCL4079ZZ-4.2

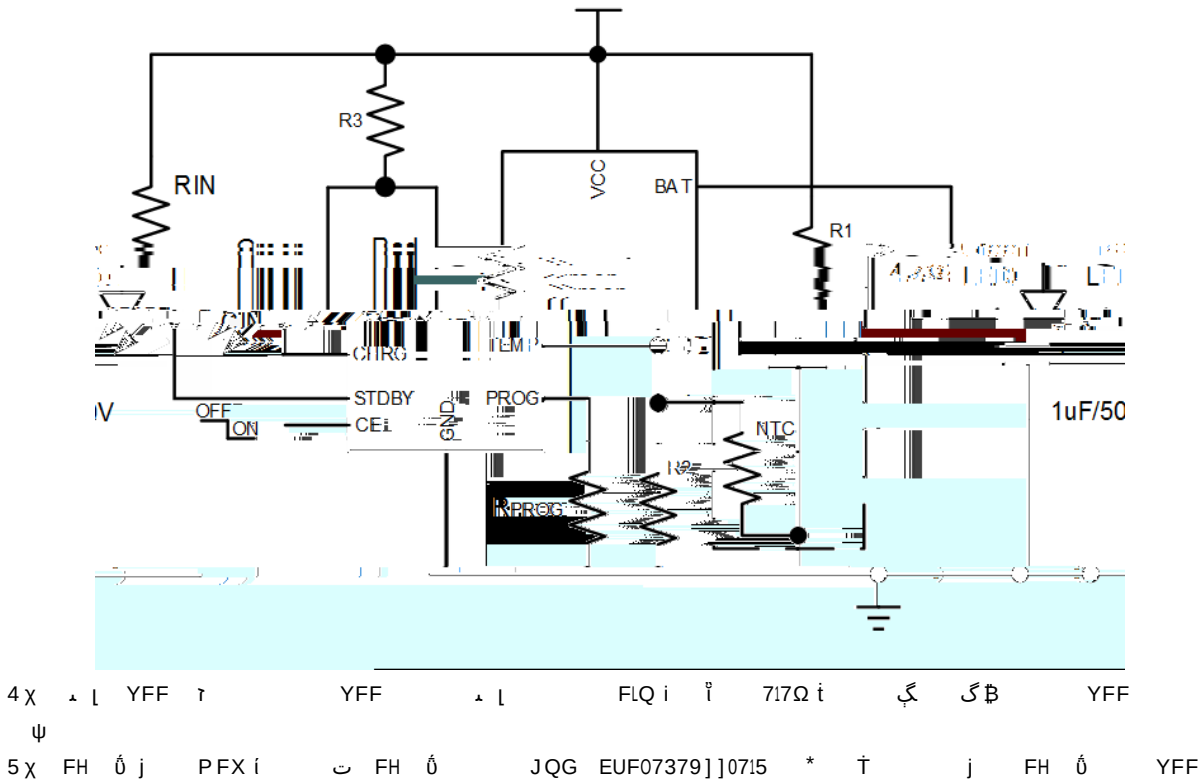
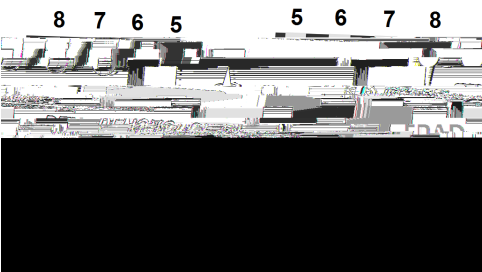
Rev.B May.-2025



蓝箭电子
BLUE ROCKET ELECTRONICS

DATA SHEET

BRCL4079ZZ-4.2	ص	خ	و	آ	خ	ي	ٴ	
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BRCL4079ZZ-4.2 آ	ج	پ		گ	USB	ب	ت	
BRCL4079ZZ-4.2 پ	ه	PMOSFET	ا	ا	ت	ا	ه	ك
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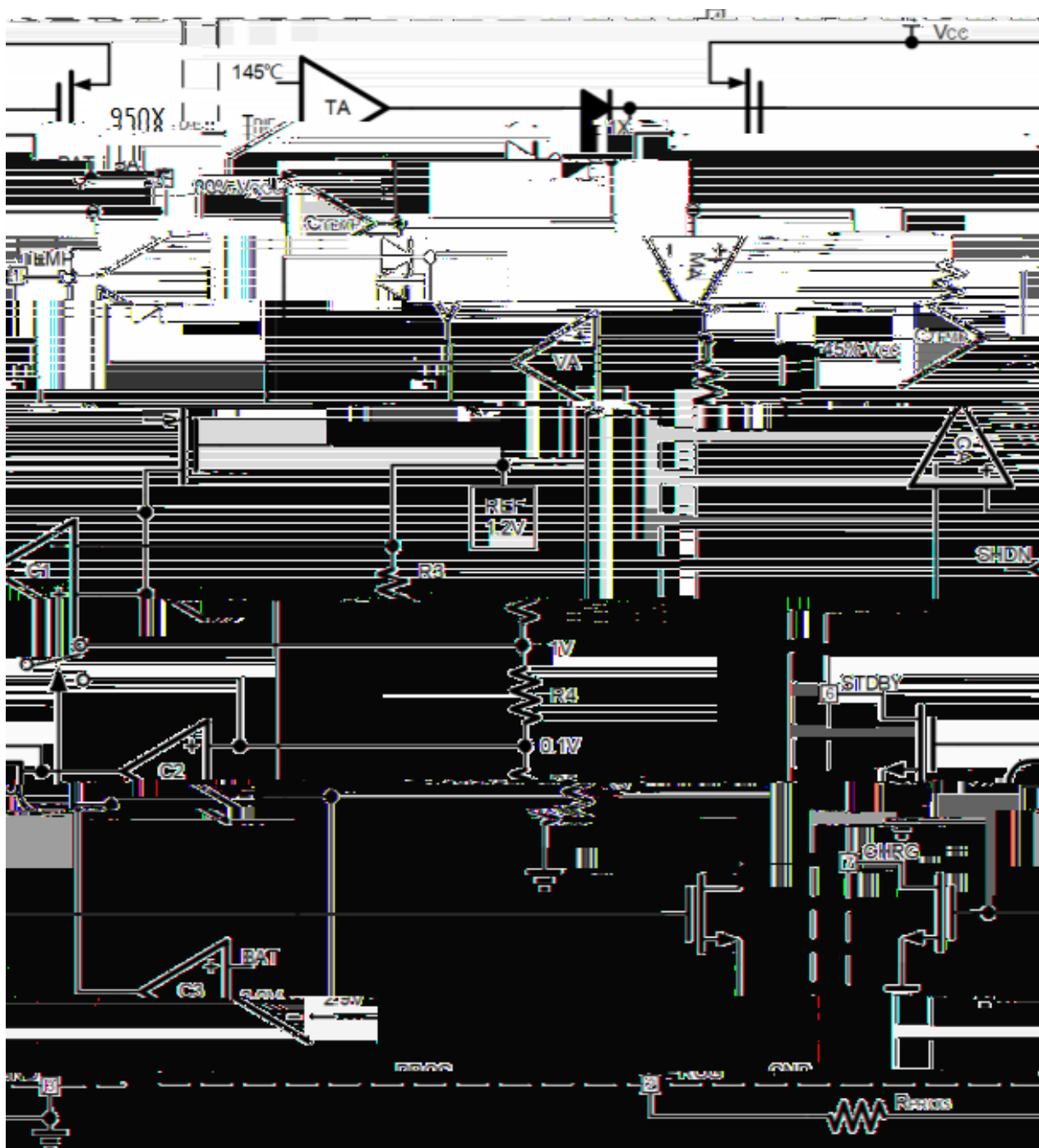
PIN Num.	Symbol	Function
1	TEMP	
2	SUR J	د ج QJ د
3	GND	ق د د
4	VCC	ج
5	BAT	ج
6	STDBY	ج ا
7	CHRG	ج
8	CE	ج ا ه ا
HSDG	JQG	ق د د

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PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
V _{CC} Under voltage Lockout Hysteresis	V _{CC_UV_HYS}	V _{CC} from High to Low	100	200	300	mV
V _{CC} -V _{BAT} Lockout Threshold	V _{ASD}	V _{CC} from Low to High	100	125	150	mV
		V _{CC} from High to Low	30	65	100	mV
C/10 Charging Termination Current	I _{TERM}	R _{PROG} =1.0k	62	95	128	mA
R _{PROG} voltage at Constant Current Mode	V _{PROG}	R _{PROG} =1.0k, Current mode	0.9	1.0	1.1	V
CHRG outputs low voltage	V _{CHRG}	I _{CHRG} =5mA		0.3	0.6	V
STDBY outputs low voltage	V _{STDBY}	I _{STDBYG} =5mA		0.3	0.6	V
Rechargeable Battery Voltage	V _{RECHRG}	V _{FLOAT} -V _{RECHRGH}	100	150	200	mV

/ Principle block diagram



$\varepsilon \approx 1.8\text{ms}$

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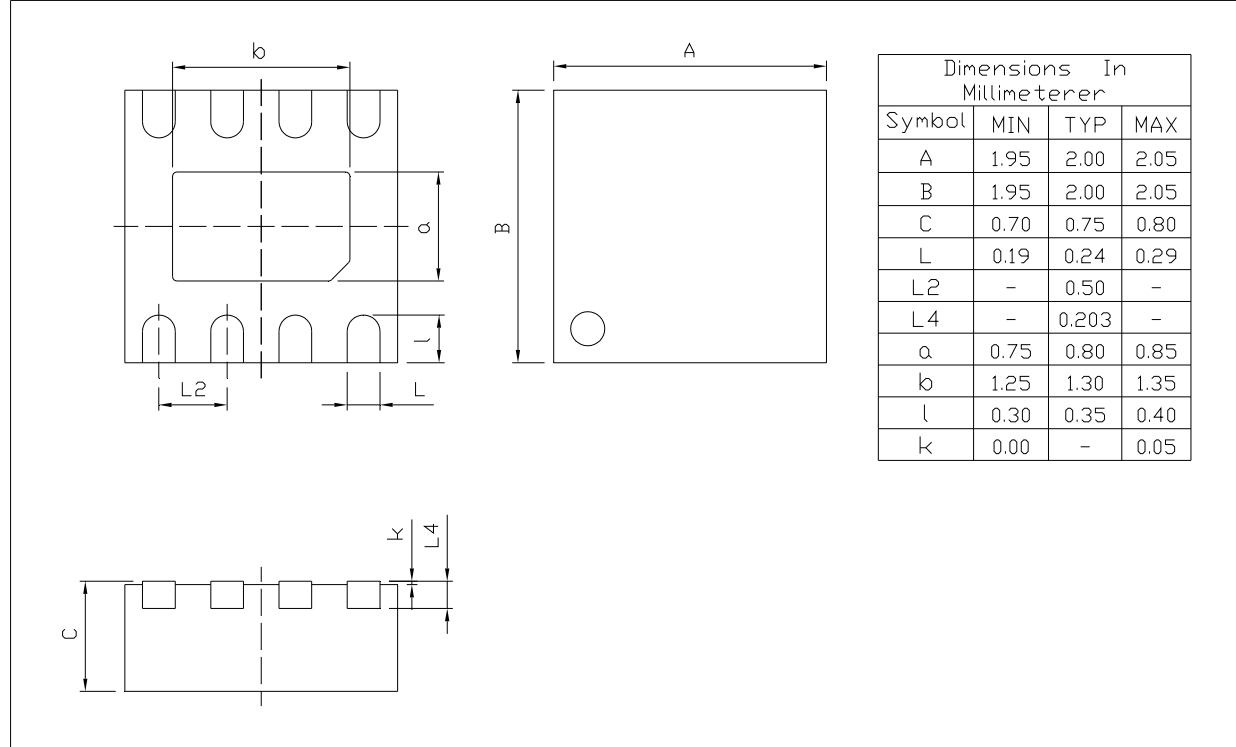
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/ Package Dimensions

DFN2×2-8L-0.75Unit:mm



Rev.00 202012

/ Marking Instructions



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7379	χ		
75	χ	٢	715Y
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- 1

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150

180

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60

90sec;
- 2

245 ° 5

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5 ° 0.5sec;
- 3

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د

2

10

/sec.
- Note:
1.Preheating:150~180 , Time:60~90sec.
2.Peak Temp.:245 ° 5 , Duration:5 ° 0.5sec.
3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

χ 260 ° 5 ج χ 10 ° 1 sec. Temp.:260±5 Time:10±1 sec

/ Packaging SPEC.

ل / REEL

Package Type	Units					Dimension (unit mm ³)		
DFN2×2-8L	4,000	10	40,000	4	160,000	7 ×8	210×205×205	445×435×230

/ Notices

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