

/ Descriptions

WR0<5+U, M M E QSQ æ M y s ll 1 Silicon NPN transistor in a TO-92(R) Plastic Package.

/ Features

$$\alpha \in \mathbb{R} \mid \mathbb{H} \cong \mathbb{H} \oplus \mathbb{H} \oplus \mathbb{H}$$

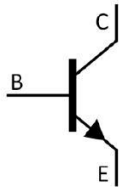
High Voltage, Low Noise.

/ Applications

$$\tilde{\mathbf{f}}_i^{\text{c}} = \tilde{\omega}_i \mathbf{z}_i \tilde{\mathbf{A}}_i \mathbf{H}_i$$

General purpose and switching application.

/ Equivalent Circuit



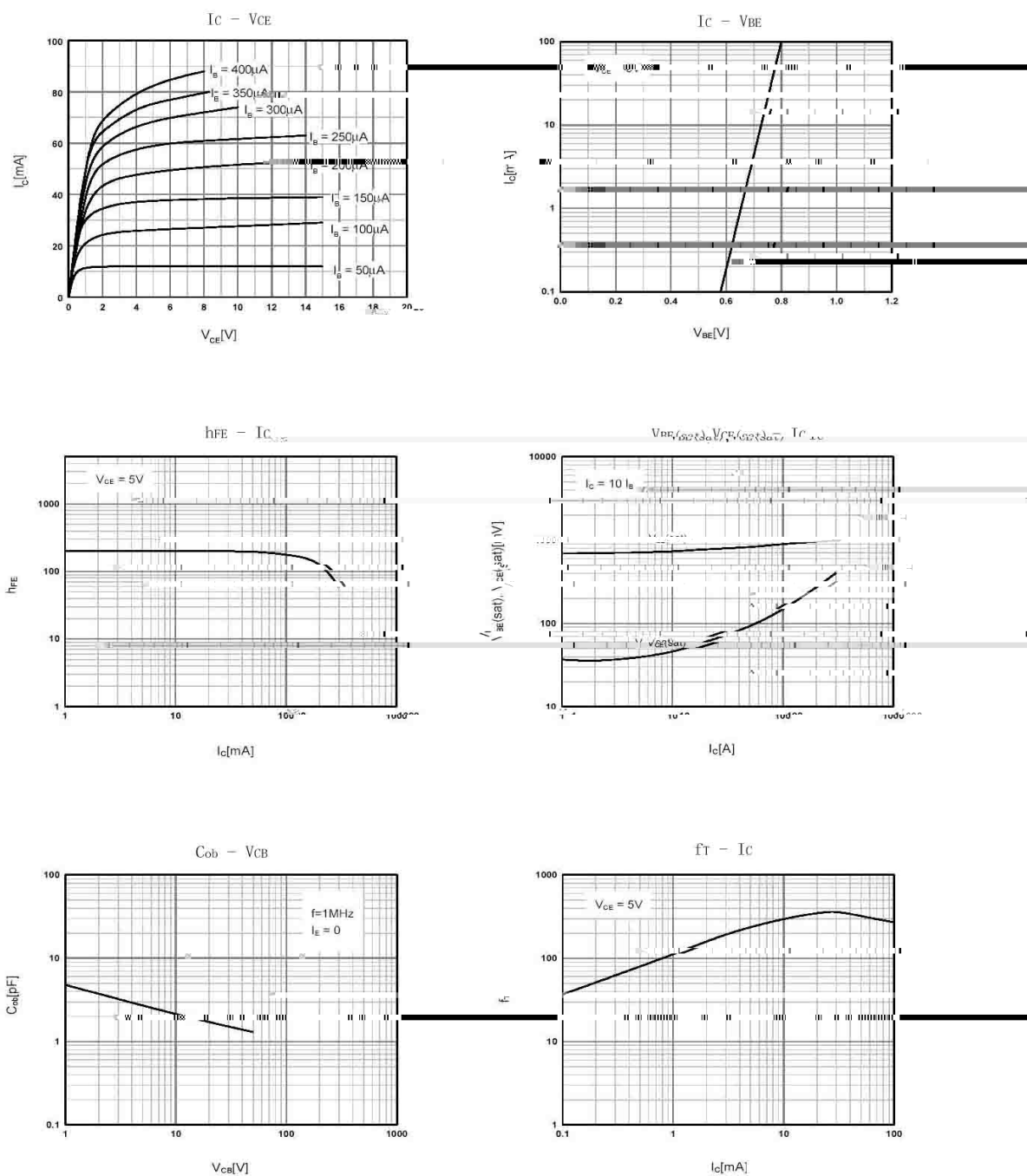
/ Pinning

PIN1 χ EmitterPIN 2 χ BasePIN 3 χ Collector

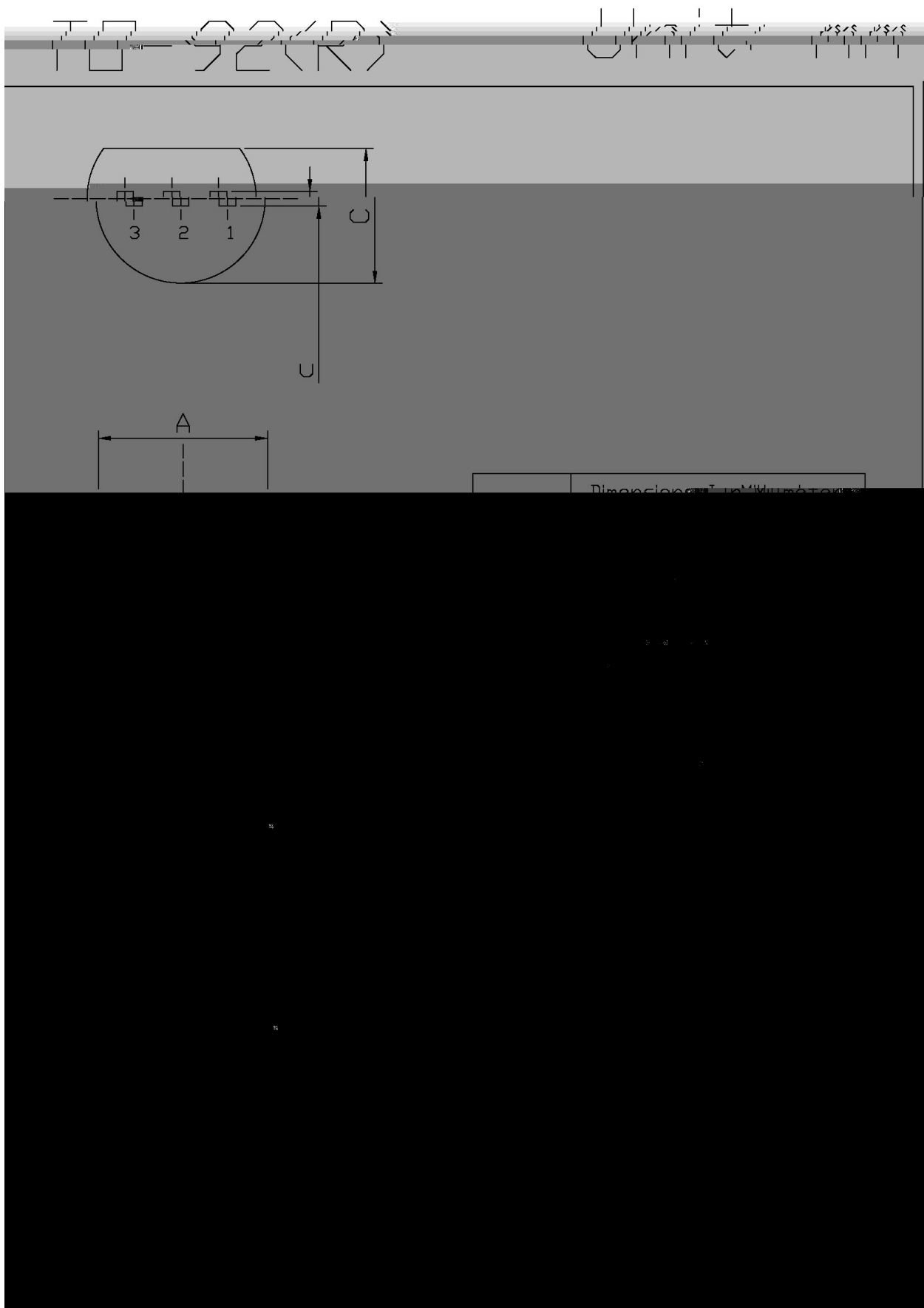
/ h_{FE} Classifications & Marking

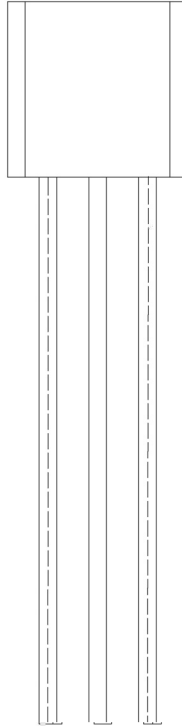
h _{FE} Classifications	A	B	C
Symbol			
h _{FE} Range			

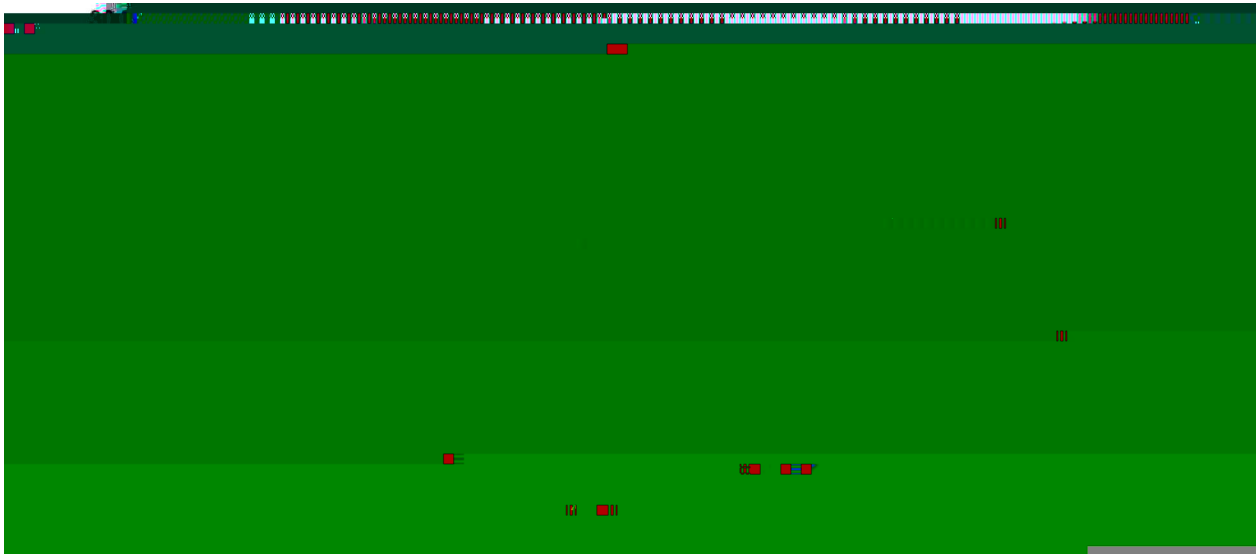
/ Electrical Characteristic Curve



/ Package Dimensions






() / Temperature Profile for Dip Soldering(Pb-Free)


б " X

1. 1. 25 K 150 t i 'H j 60 K 90sec;

2. 2. 255 ° 5 t i 'H j 'Y 1 K 5 ° 0.5sec;

3. 3. i ' Y, c h y K 2 K 10 t /sec.

Note:

1.Preheating:25~150 t , Time:60~90sec.

2.Peak Temp.:255 ° 5 t , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 t /sec.

/ Resistance to Soldering Heat Test Conditions

□ y X 270 ° 5 t

'H j X 10 ° 1 sec.

Temp.:270±5

Time:10±1 sec

/ Packaging SPEC.

"Q T Y E / BULK

Package Type	Units					Dimension		(unit mm3)

1 V Y E / AMMO

Package Type	Units					Dimension		(unit mm3)

/ Notices