

/ Descriptions

WR0585 ММ Ё SQS æ Ñ ç s ll 1 Silicon PNP transistor in a TO-252 Plastic Package.

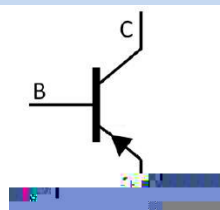
/ Features

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Low $V_{CE(sat)}$, excellent DC current gain characteristics, complements the 2SD2118.

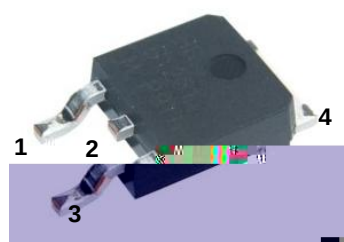
/ Applications

т ³ ² А ı ' ½ ²
Medium power amplifier applications.

/ Equivalent Circuit



/ Pinning



PIN1 χ Base PIN 2,4 χ Collector PIN 3 χ Emitter

/ h_{FE} Classifications & Marking

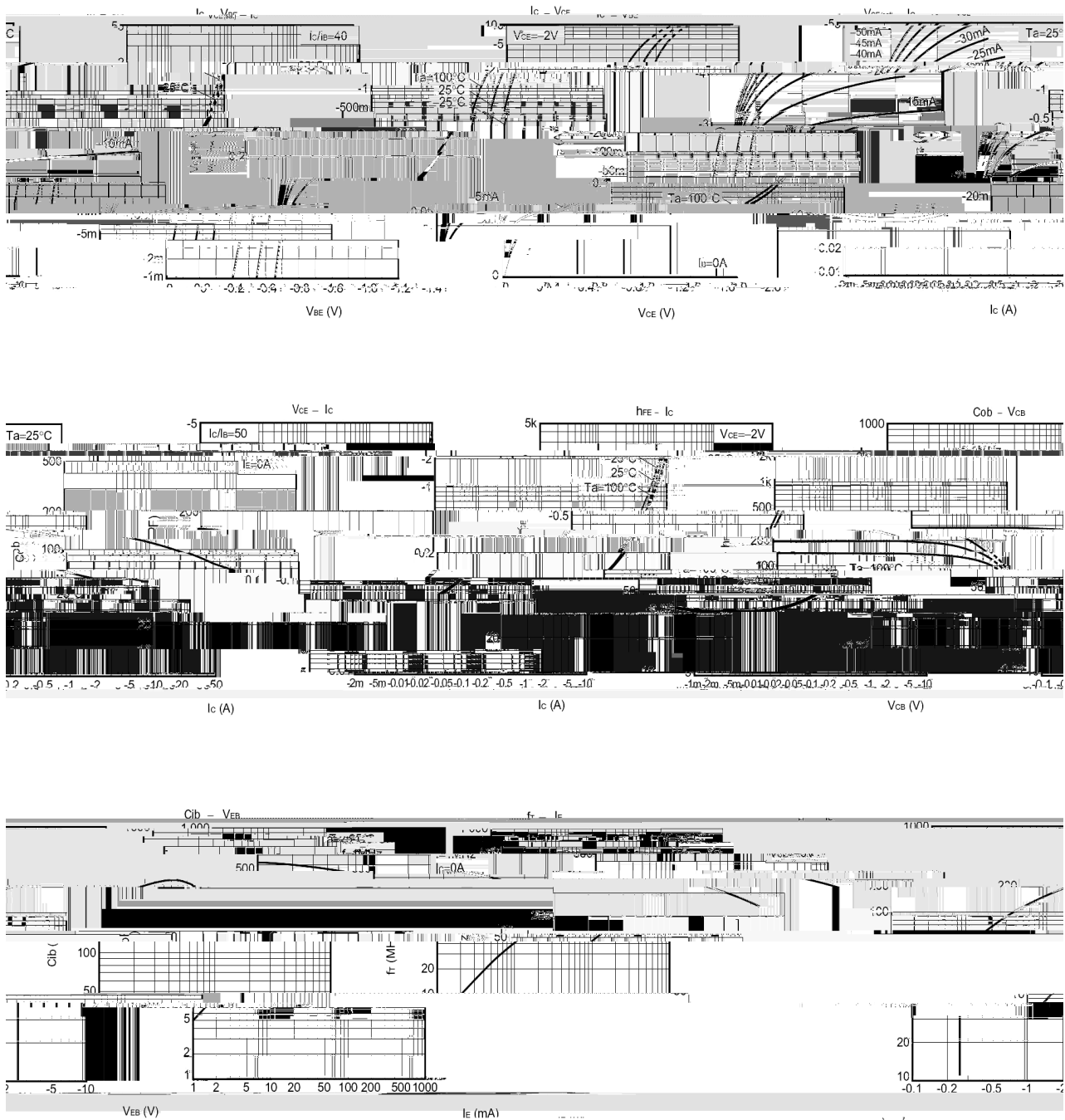
h_{FE} Classifications Symbol	P	Q	R
h_{FE} Range	82 180	120 270	180 390

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-30	V
Collector to Emitter Voltage	V_{CEO}	-20	V
Emitter to Base Voltage	V_{EBO}	-6.0	V
Collector Current - Continuous	I_C	-5	A
Peak Collector Current – Continuous	I_{CM}	-10	A
Collector Power Dissipation	P_C	1.0	W
Collector Power Dissipation*	$*P_C(T_c=25^\circ C)$	10	W
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 ~ 150	

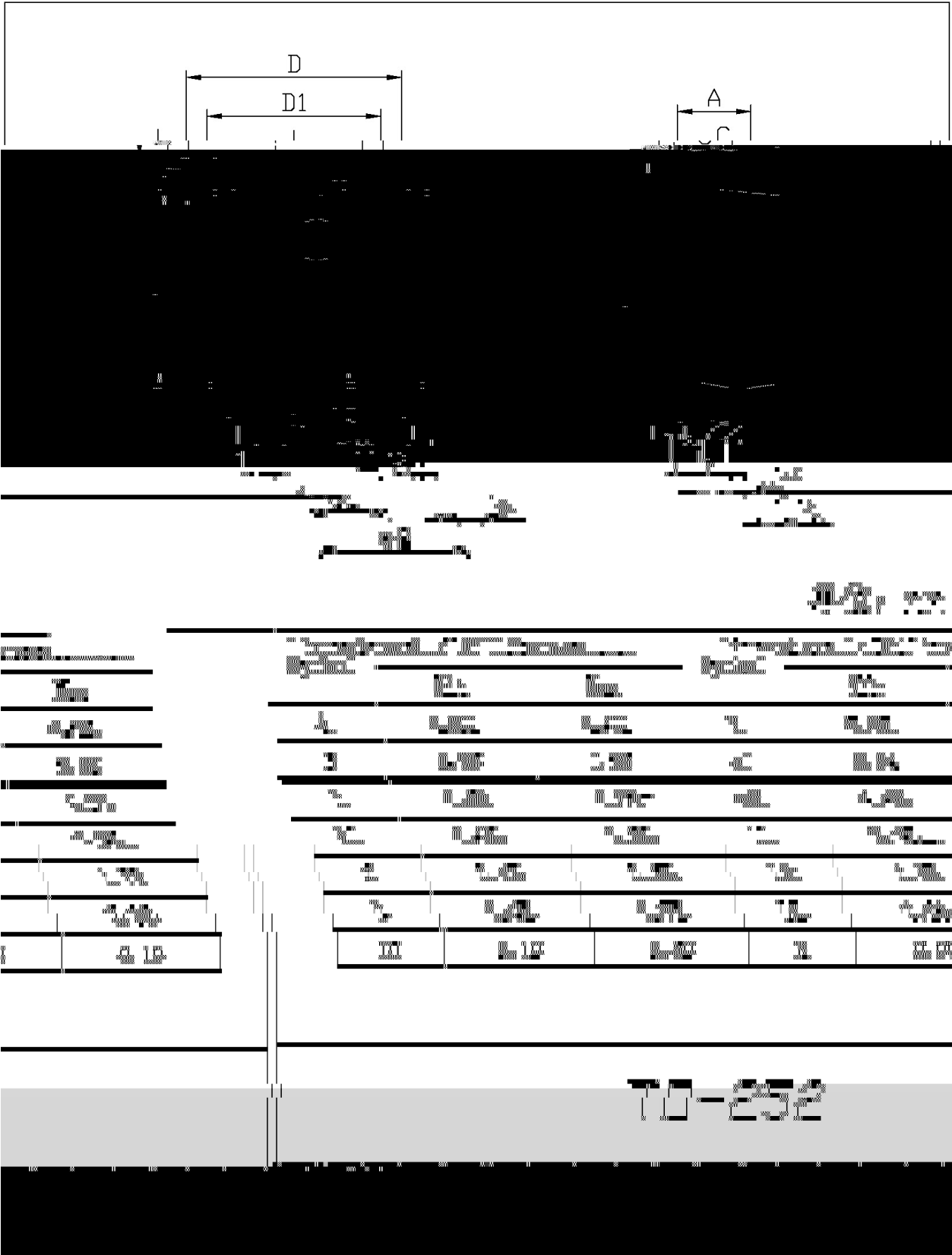
*mounted On40×40×0.7mm ceramic board.
- 73Ö73Ö31: p p — i

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Base Breakdown Voltage	V_{CBO}	$I_C=-50\mu A$ $I_E=0$	-30			V
Collector to Emitter Breakdown Voltage	V_{CEO}	$I_C=-1.0mA$ $I_B=0$	-20			V

/ Electrical Characteristic Curve



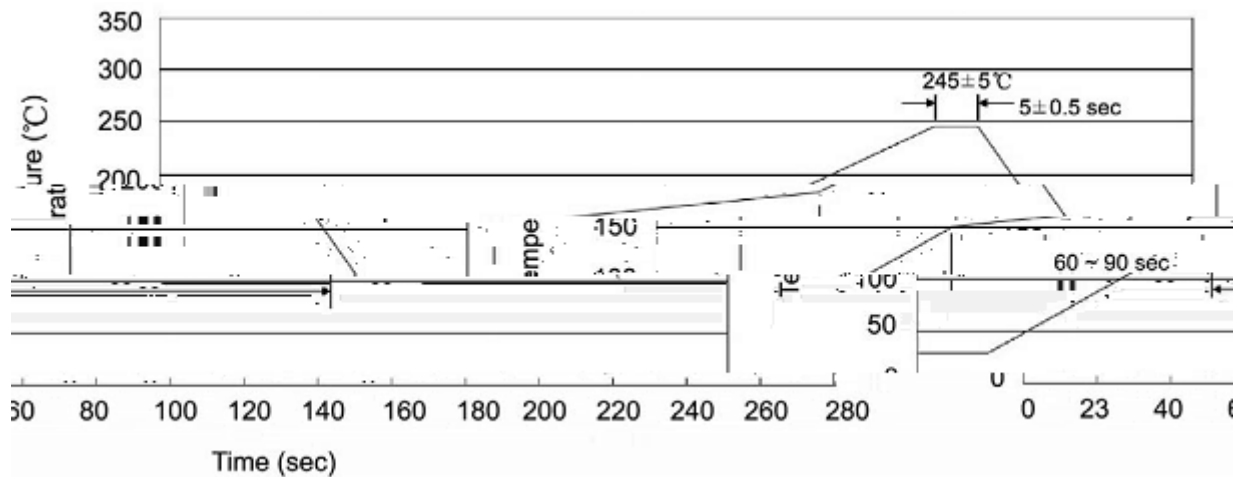
/ Package Dimensions



A technical line drawing of a rectangular box. The box has a handle on top, represented by a trapezoidal shape. On the front face, there is a large circle. The box is supported by four vertical legs, each with a dashed line indicating a central hole or channel. A small rectangular protrusion is located on the bottom edge of the front face.

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℃

1. 25~150℃, Time: 60~90sec;
2. 245±5℃, Duration: 5±0.5sec;
3. Cooling Speed: 2~10℃/sec.

Note:

1. Preheating: 25~150℃, Time: 60~90sec.
2. Peak Temp.: 245±5℃, Duration: 5±0.5sec.
3. Cooling Speed: 2~10℃/sec.

/ Resistance to Soldering Heat Test Conditions

260±5℃, Time: 10±1 sec. Temp.: 260±5 Time: 10±1 sec

/ Packaging SPEC.

REEL

Package Type	Units				Dimension (unit mm ³)		