

/ Descriptions

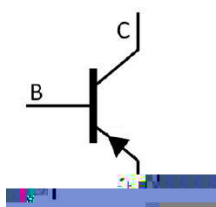
WR0<5 ММ Ё SQS æ Ñ ы s ll 1 Silicon PNP transistor in a TO-92 Plastic Package.

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Low $r_{bb'}$, low noise figure, complementary pair with 2SC3329

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Low noise audio amplifier applications.



PIN1 χ Base

PIN 2 χ Collector

PIN 3 χ Emitter

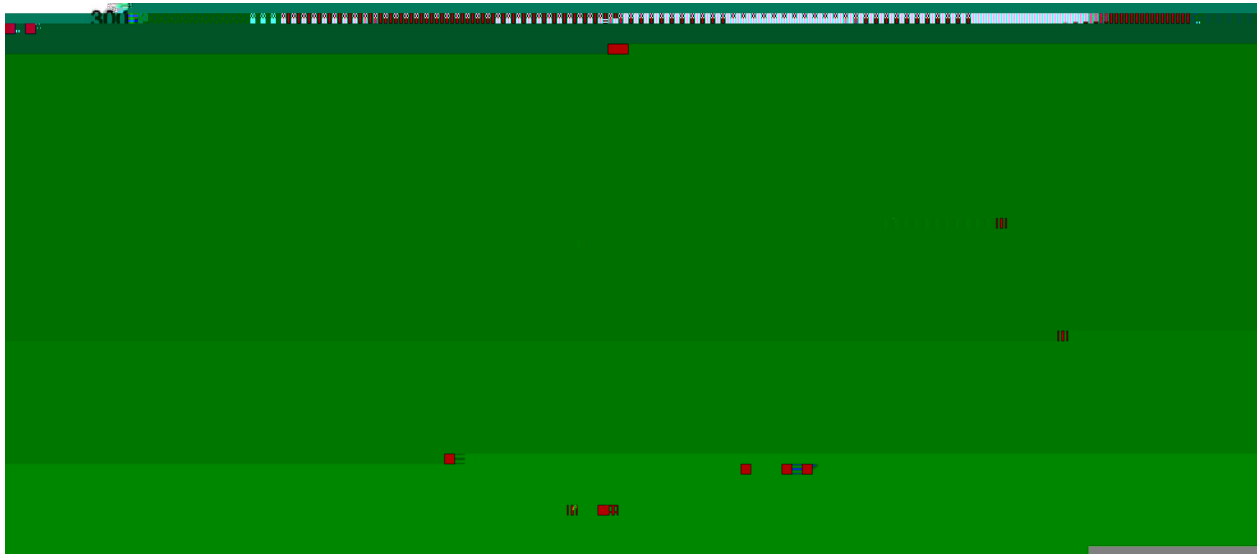
/ hFE Classifications & Marking

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-80	V
Collector to Emitter Voltage	V_{CEO}	-80	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-100	mA
Base Current - Continuous	I_B	-20	mA
Collector Power Dissipation	P_C	400	mW

2SA1316

2SA1316
Rev.E Mar.-2016

() / Temperature Profile for Dip Soldering(Pb-Free)



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1. 1. 25~150 °C, Time: 60~90sec;
2. 2. 255 °C, Duration: 5 ° 0.5sec;
3. 3. Cooling Speed: 2~10 ° /sec.

Note:

1. Preheating: 25~150 °C, Time: 60~90sec.
2. Peak Temp.: 255 °C, Duration: 5 ° 0.5sec.
3. Cooling Speed: 2~10 ° /sec.

/ Resistance to Soldering Heat Test Conditions

□ ý χ 270 ° 5 ± °H ж χ 10 ° 1 sec. Temp: 270 ± 5 Time: 10 ± 1 sec

/ Packaging SPEC.

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