

2N6124

Rev.H Oct.-2018



DATA SHEET

TO-220

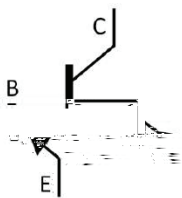
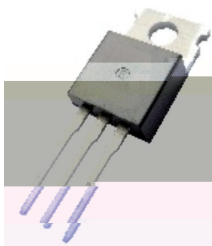
PNP

Silicon PNP transistor in a TO-220 Plastic Package.

2N6121

Complement to 2N6121.

Medium power linear switching applications.



PIN1 Base

PIN 2 Collector

PIN 3 Emitter

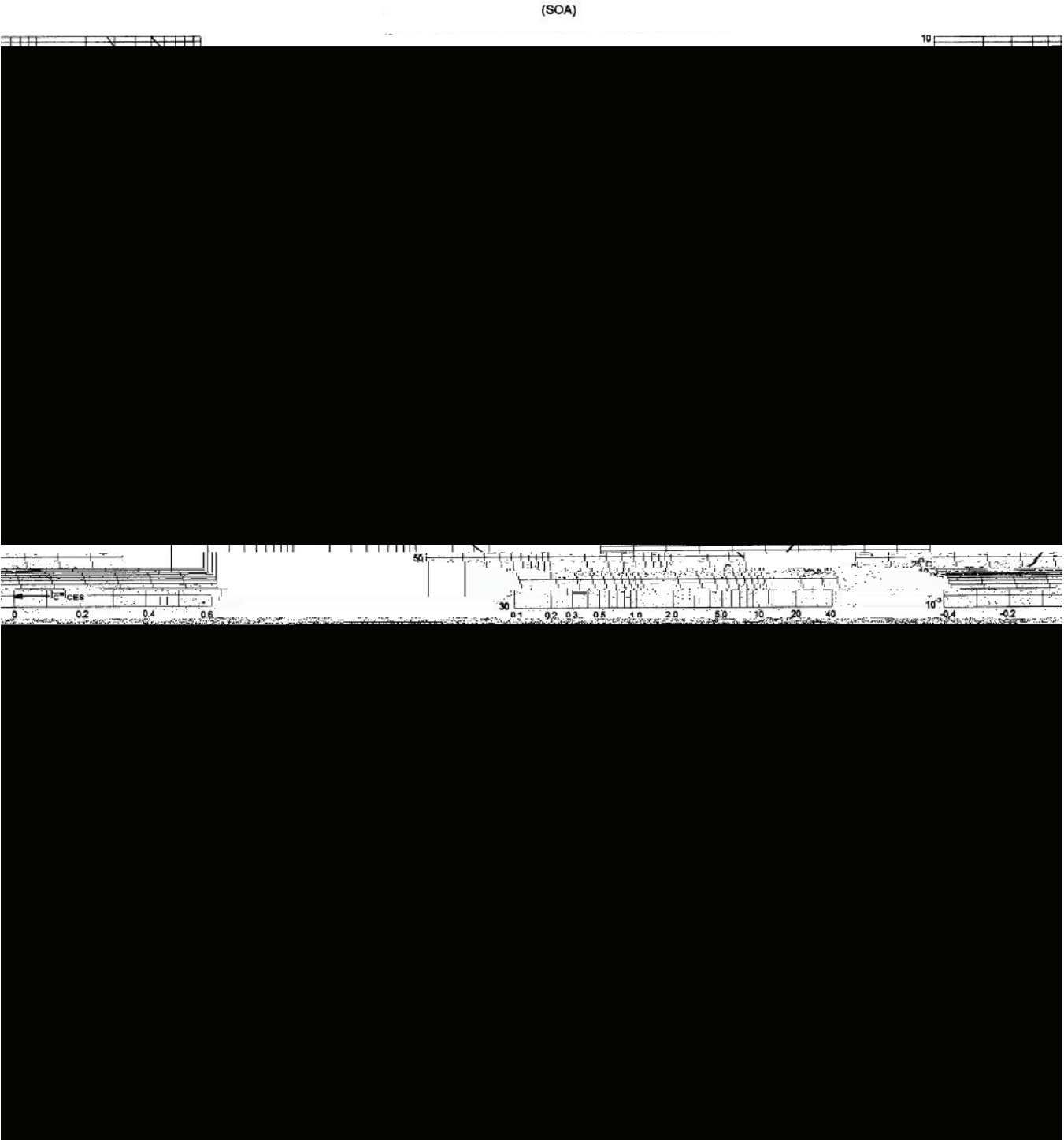
/ h_{FE} Classifications & Marking

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-45	V
Collector to Emitter Voltage	V_{CEO}	-45	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-4.0	A
Peak Collector Current	I_{CM}	-7.0	A
Base Current - Continuous	I_B	-1.0	A
Collector Power Dissipation	$P_C(T_C=25^\circ\text{C})$	40	W
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

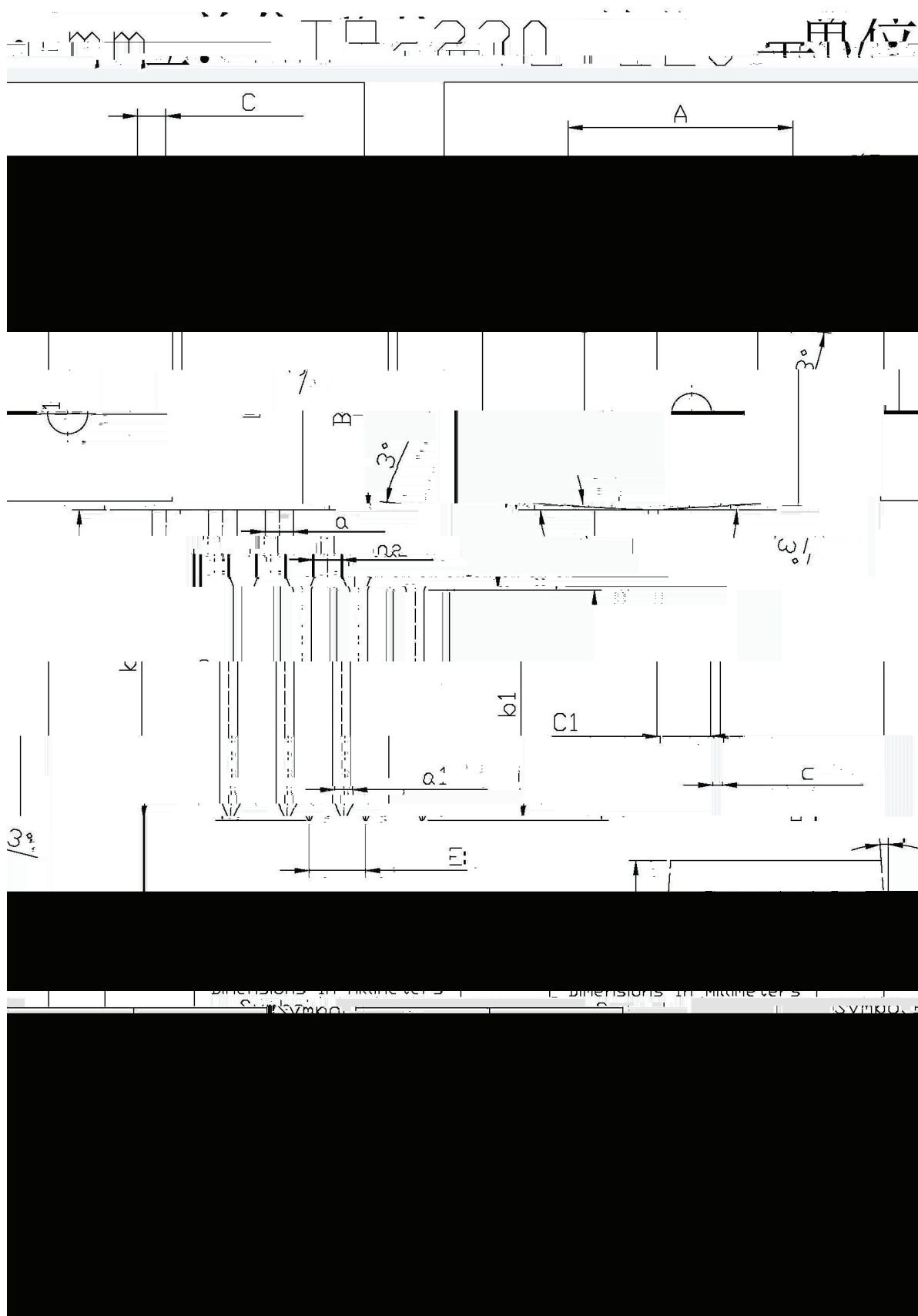
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Base Breakdown Voltage*	$*V_{CEO}$	$I_C=-100\text{mA}$ $I_B=0$	-45			V

Tc2.086

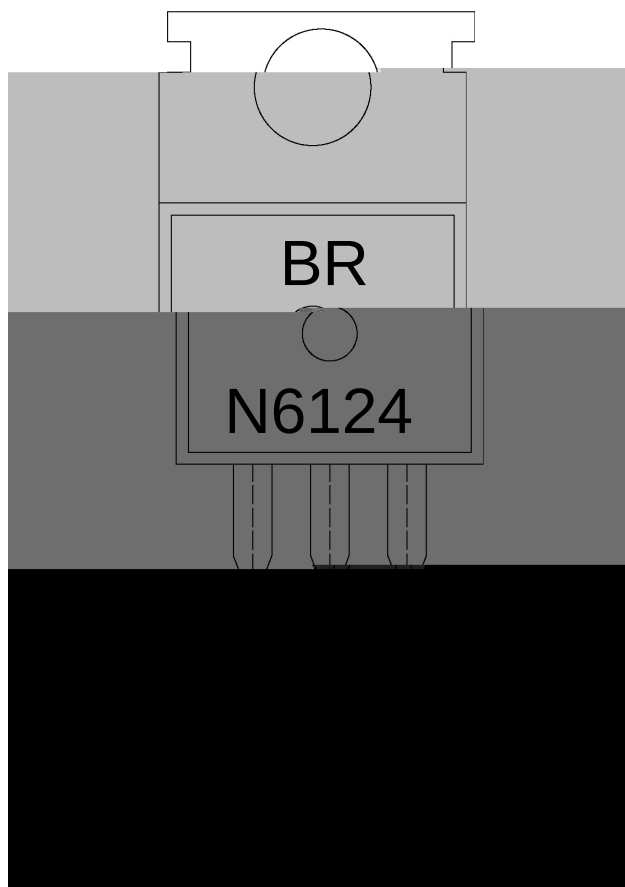
/ Electrical Characteristic Curve



/ Package Dimensions



/ Marking Instructions



BR

N6124

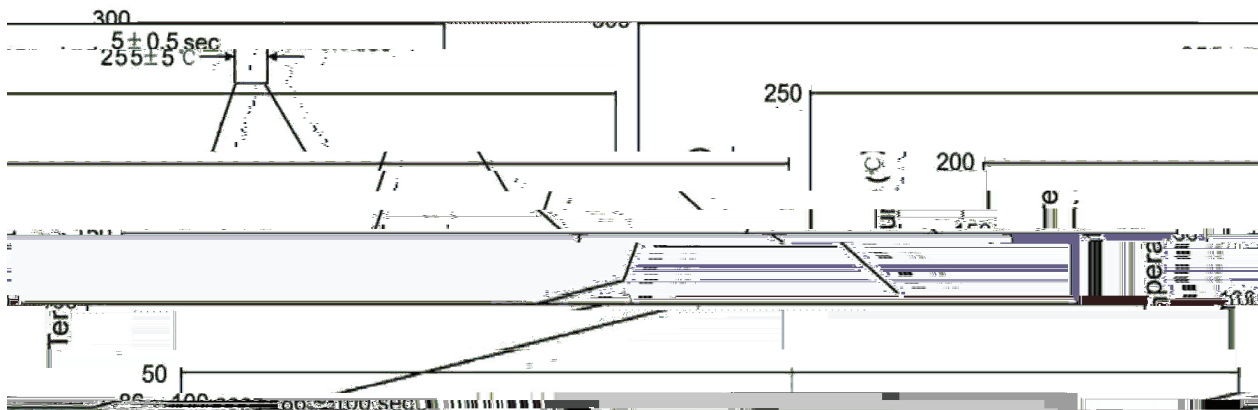
Note:

BR: Company Code

N6124: Product Type.

****: Lot No. Code, code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- | | | | | | |
|---|-------|-----|-----------|---|--------------------------------------|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 255±5 | | 5±0.5sec; | 2.Peak Temp.:255±5 , Duration:5±0.5sec. | |
| 3 | | 2 | 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

/ Resistance to Soldering Heat Test Conditions

270±5 10±1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

/ BULK

Package Type	Units					Dimension (unit mm ³)		

/ TUBE

Package Type	Units					Dimension (unit mm ³)		

/ Notices